



REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPR'D
	9	RELEASED TO STATUS G, #2005222	6/23/03	NICOLAY
	10	GCN #2005856	9/15/03	NICOLAY
	A	RELEASED TO STATUS A, #2006240	11/5/03	NICOLAY
	B	ECO #2007139	2/10/04	NICOLAY

4535 670 05941

RHOST SG164

SCHEMATIC DIAGRAM

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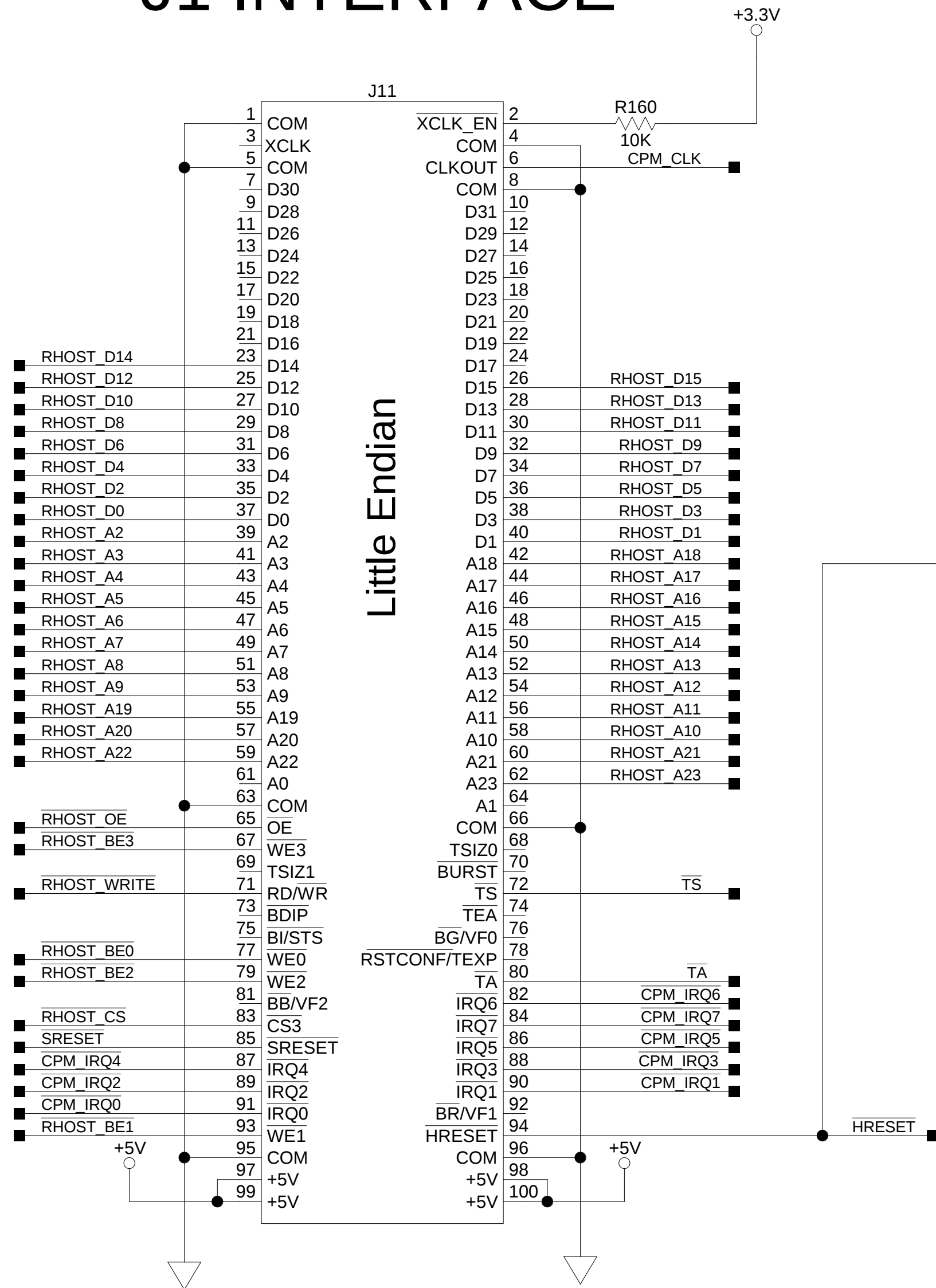
NEXT ASSY	QTY	DWN	DN/KC	2/3/03	PHILIPS MEDICAL SYSTEMS (CLEVELAND), INC. ©2003 CLEVELAND, OHIO 44143 U.S.A.					
4535 670 17741	REF	CHK	D.HOFFMAN	6/20/03						
END ITEM		ENG	D.NICOLAY	6/20/03	SCHEMATIC RHOST SG164					
4535 670 05721		MFG	ANGELINE	6/23/03						
MATERIAL										
FINISH					REL. NO.	D	4535 670 05941		B	
					DWG. SCALE					NONE
					DO NOT SCALE					
							SHEET	1	OF 20	REV



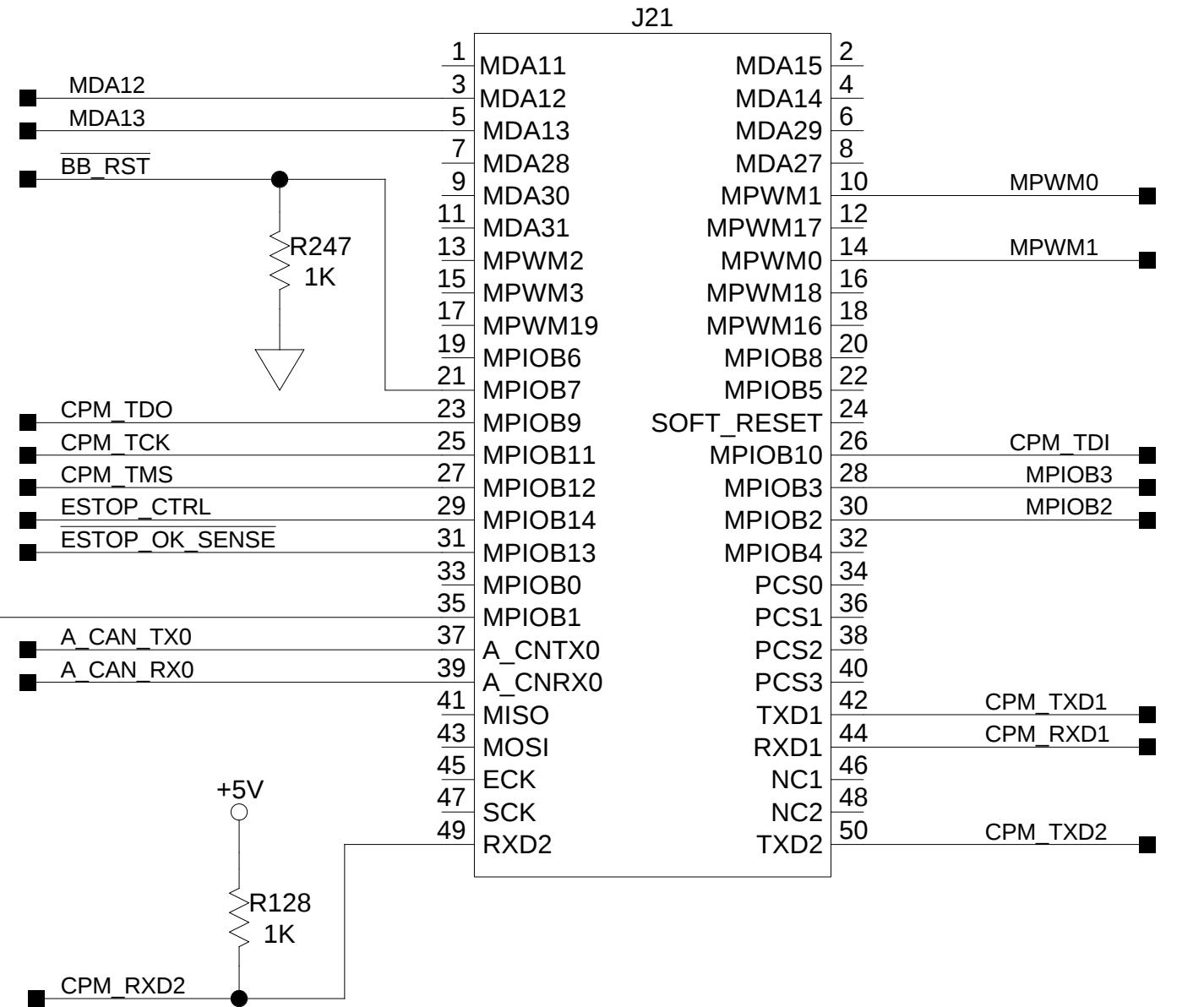
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D

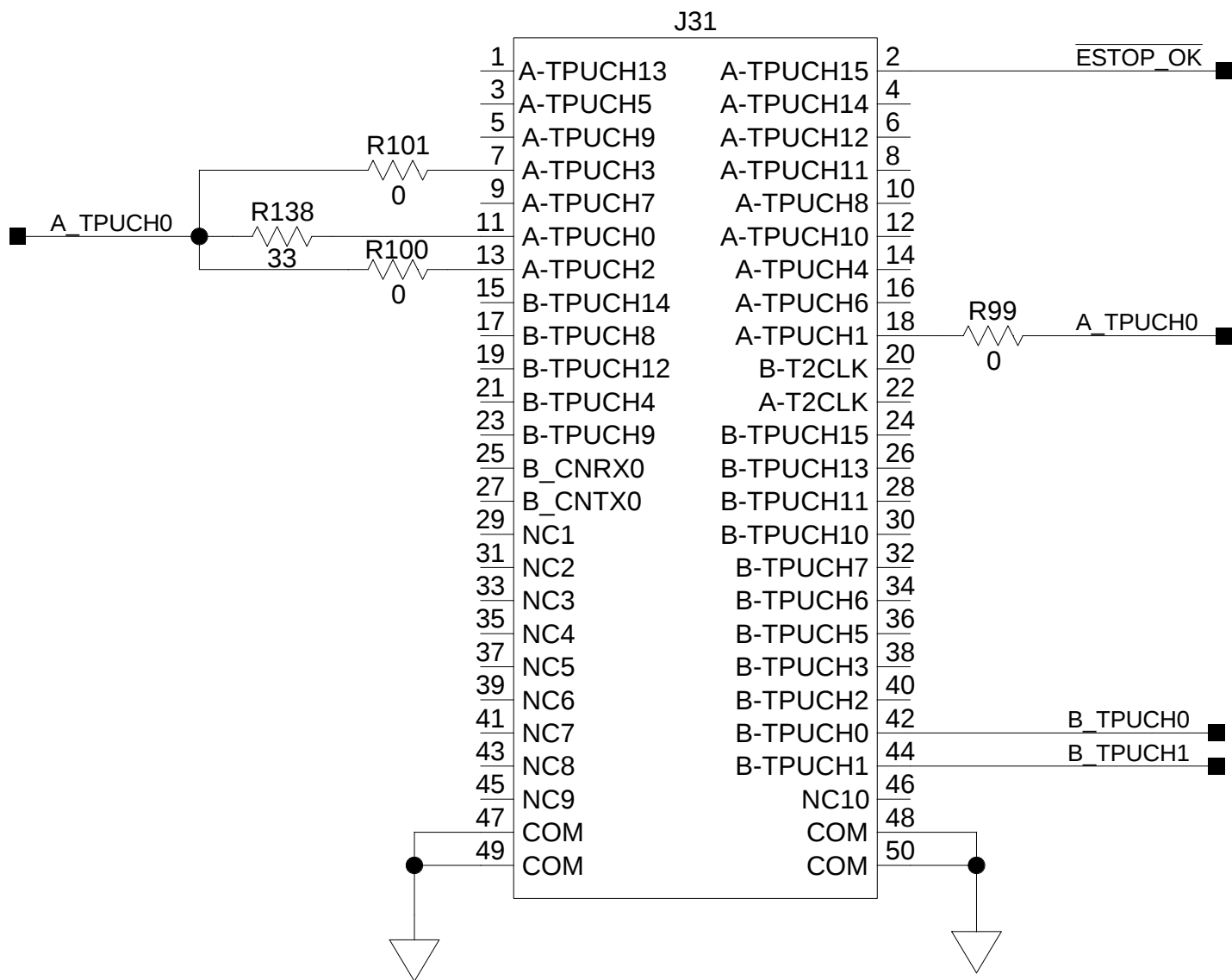
J1 INTERFACE



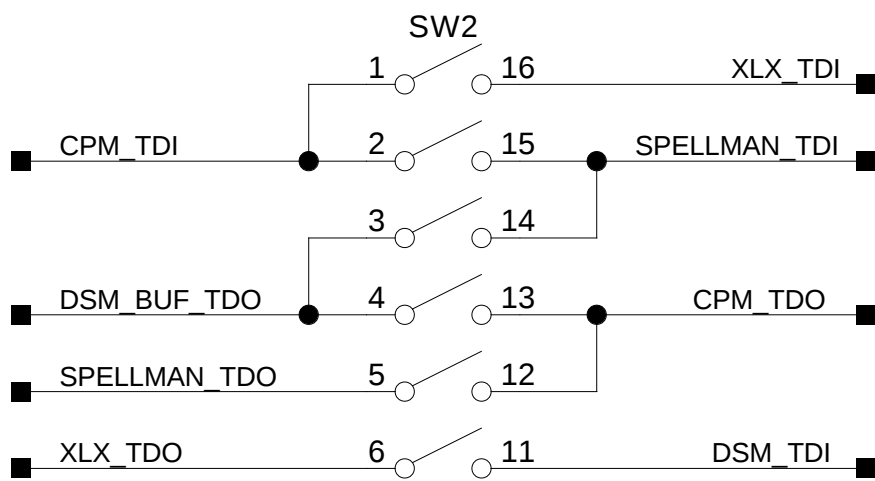
J2 INTERFACE



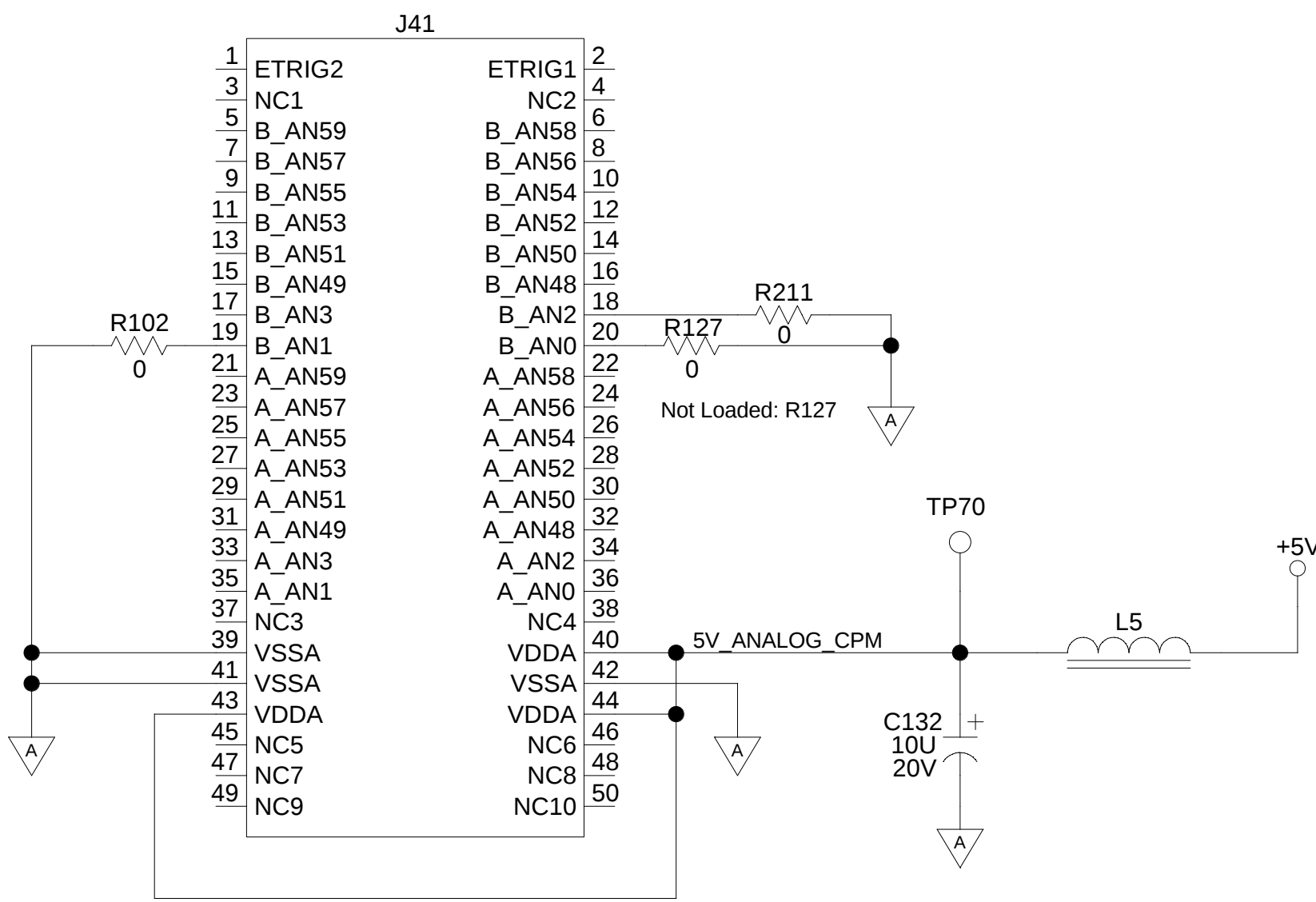
J3 INTERFACE



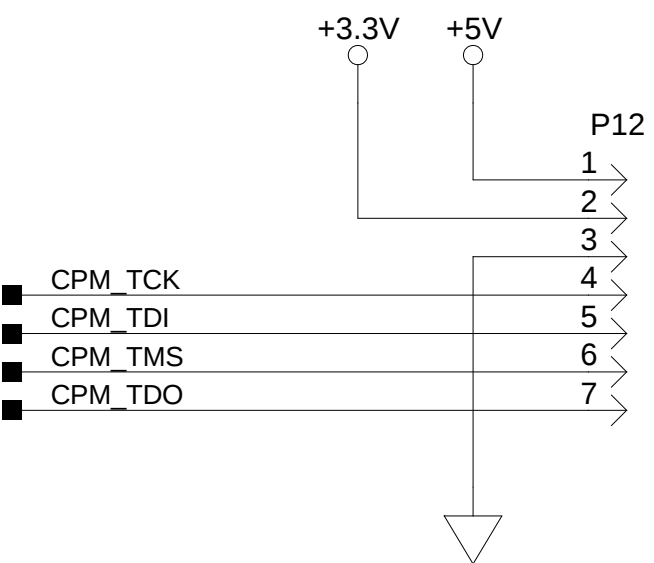
JTAG CHAIN



J4 INTERFACE

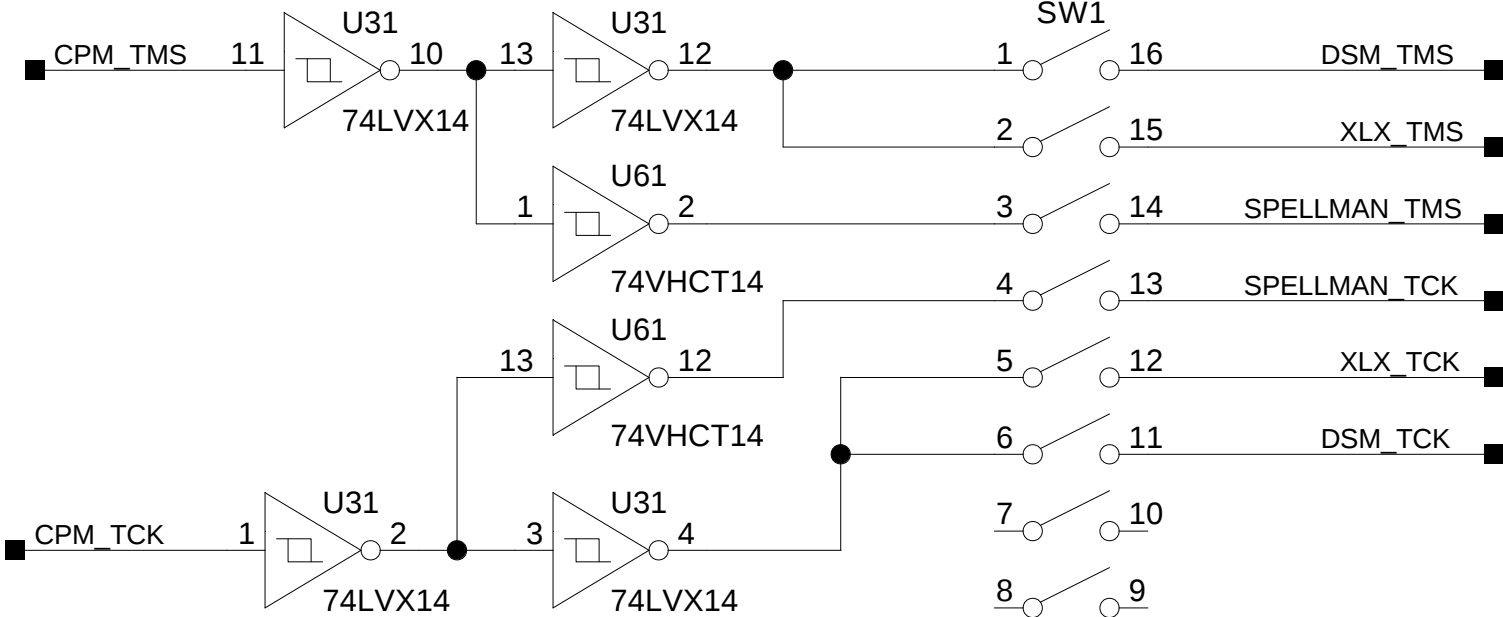


CPM JTAG



SWITCH SETTING INSTRUCTIONS

- 1.) FOR MANUAL ONLY PROGRAMMING ALL SWITCHES ON SW1 AND SW2 ARE OPEN.
- 2.) FOR CPM JTAG PROGRAMMING SW2-1,SW2-4, SW2-6,SW1-1,SW1-2,SW1-5,SW1-6 ARE CLOSED, ALL OTHERS ARE OPEN. (ALLOWS MANUAL PROGRAMMING OF RHOST VIA P12 AND MANUAL PROGRAMMING OF GENERATOR VIA P2)



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DO NOT SCALE			SHEET 2 OF 20	REV

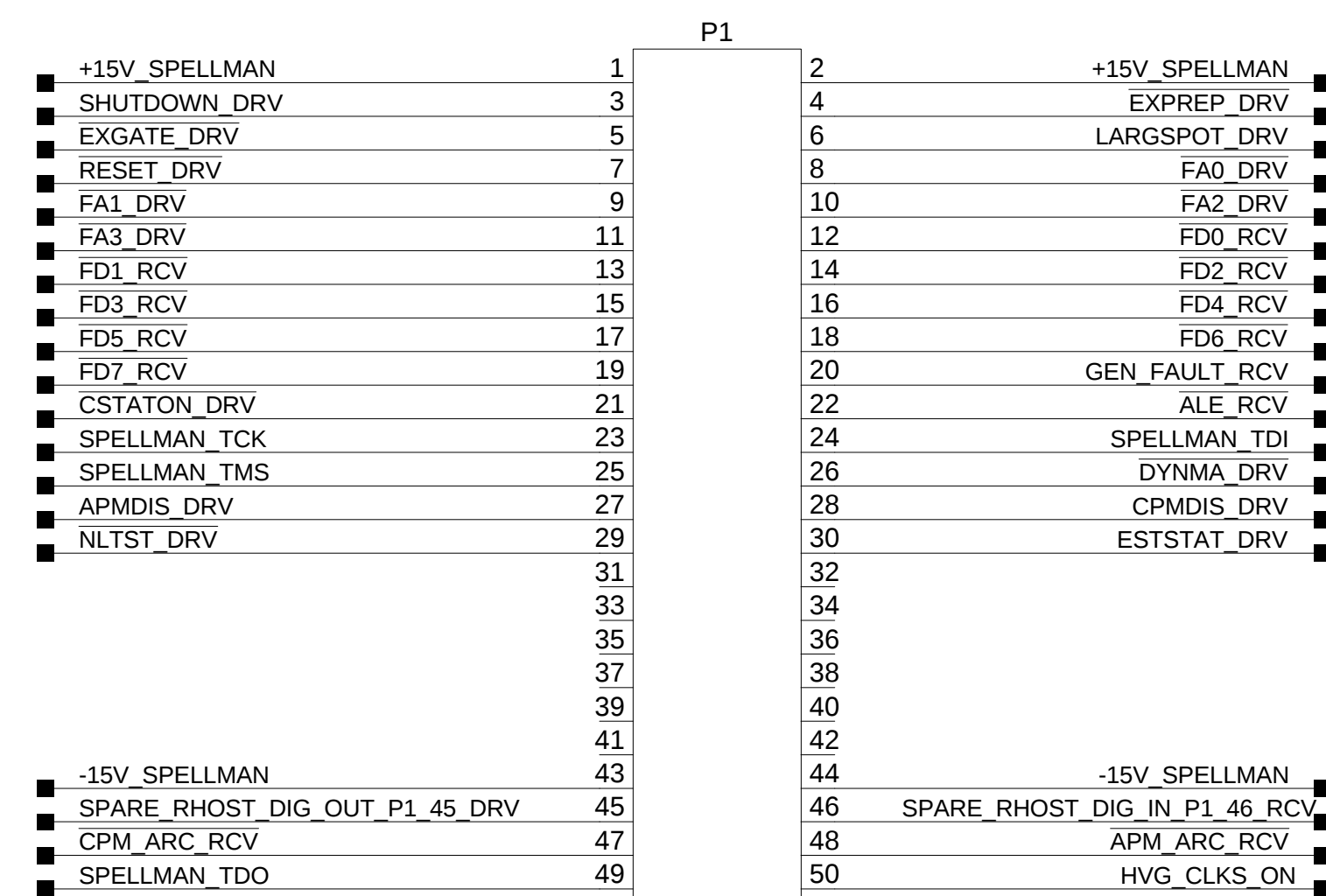
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1 453567005941_REV_B 2

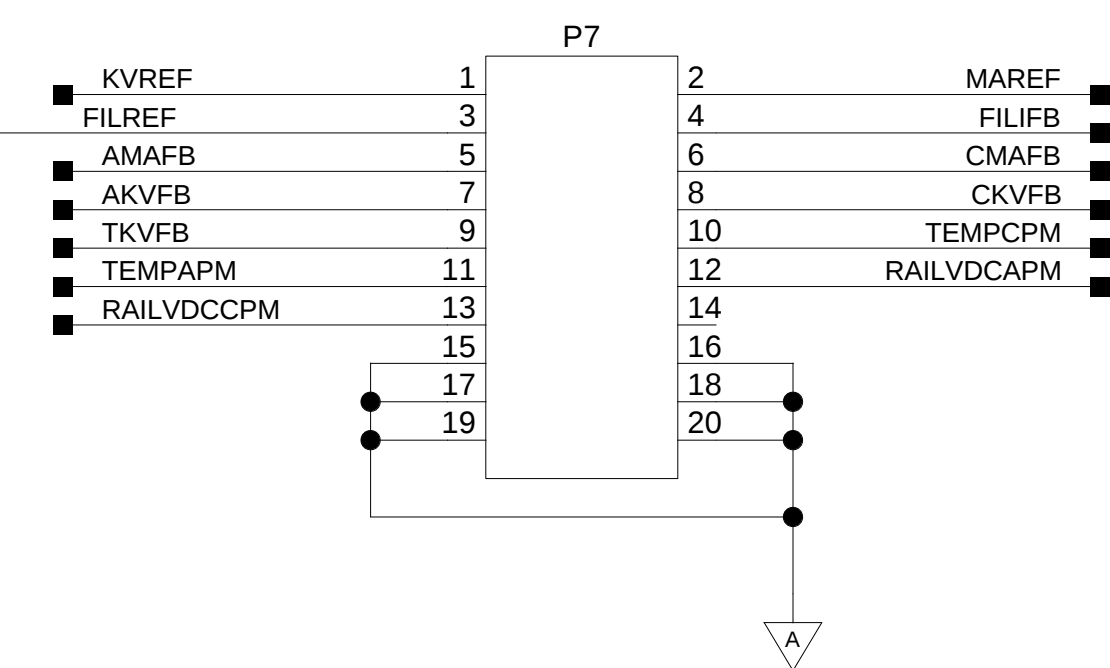
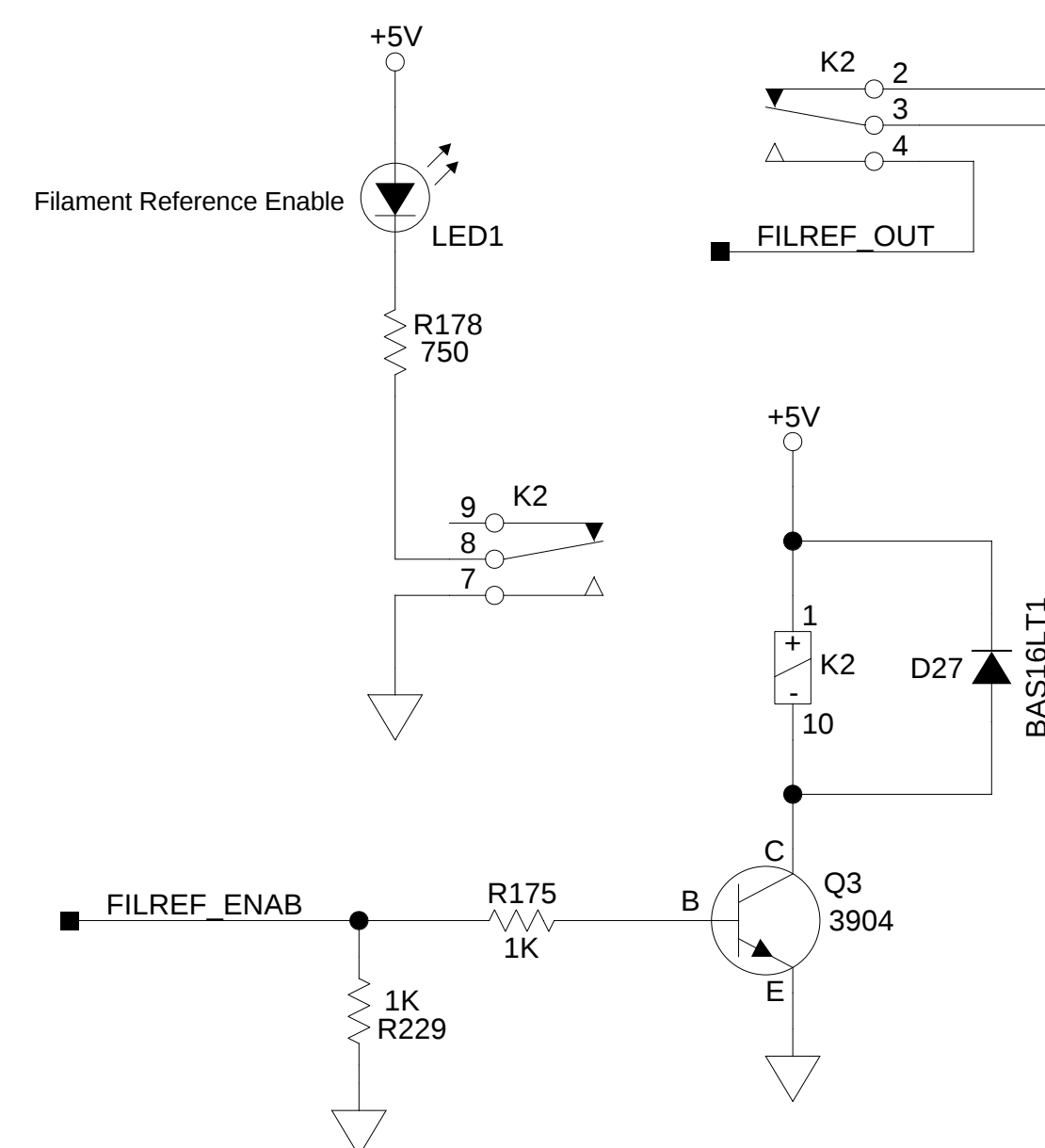
A

HIGH VOLTAGE GENERATOR INTERFACE

HIGH VOLTAGE GENERATOR DIGITAL



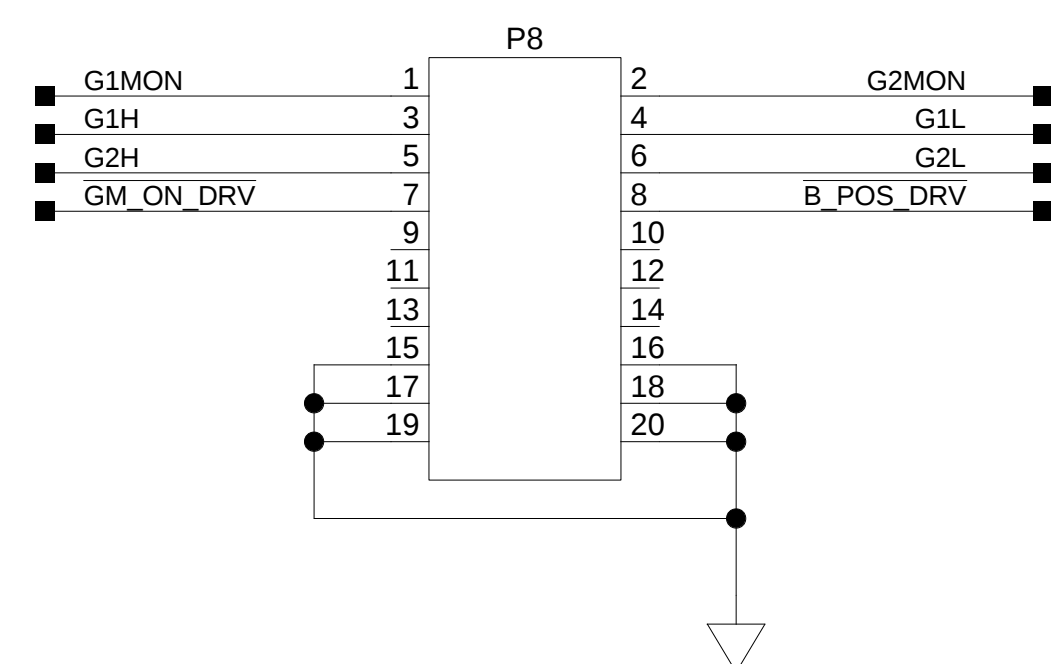
Uncontrolled Impedance Connector



Uncontrolled Impedance Connector

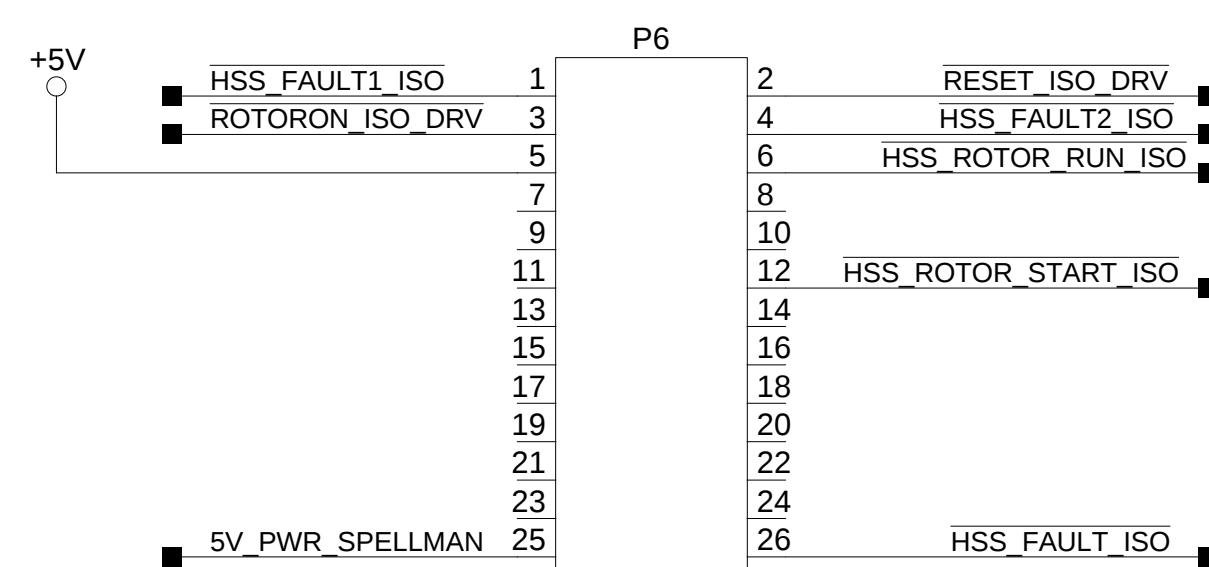
HIGH VOLTAGE GENERATOR ANALOG

GRID MODULATOR



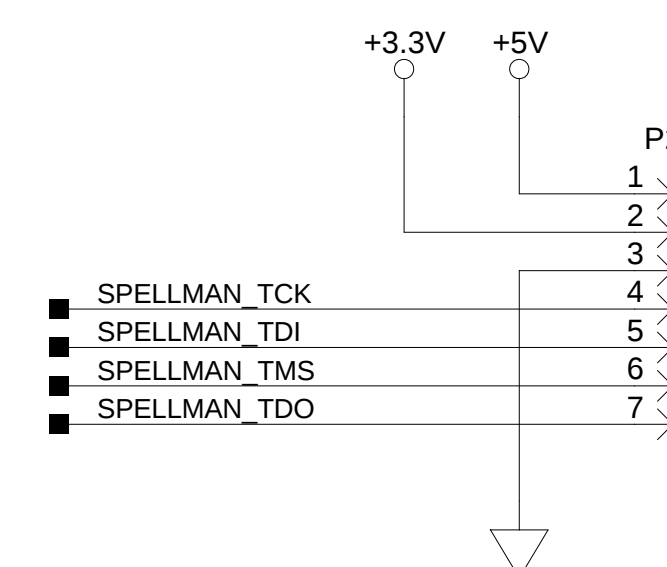
Uncontrolled Impedance Connector

HIGH SPEED STARTER



Uncontrolled Impedance Connector

HVG JTAG



D

C

B

A

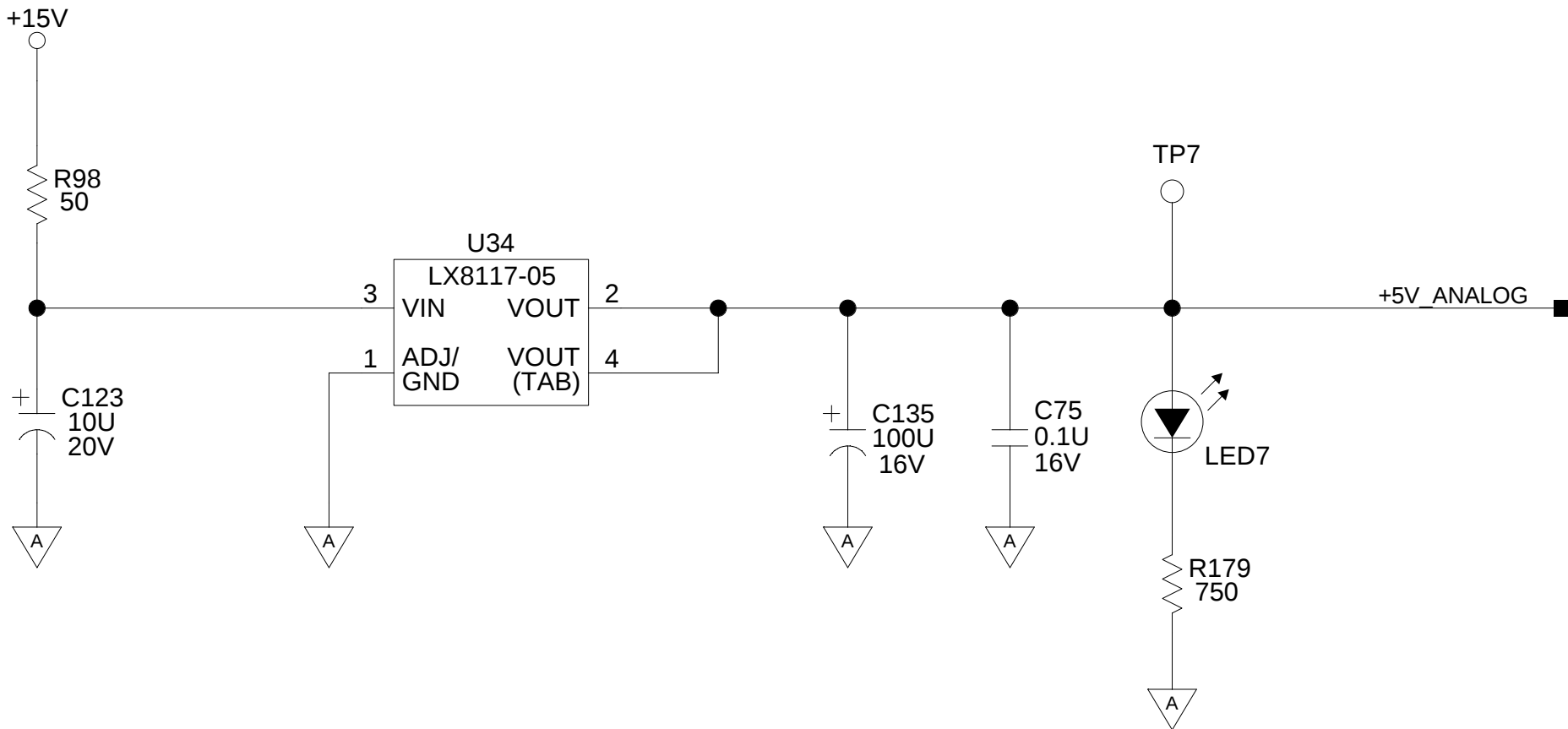
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C

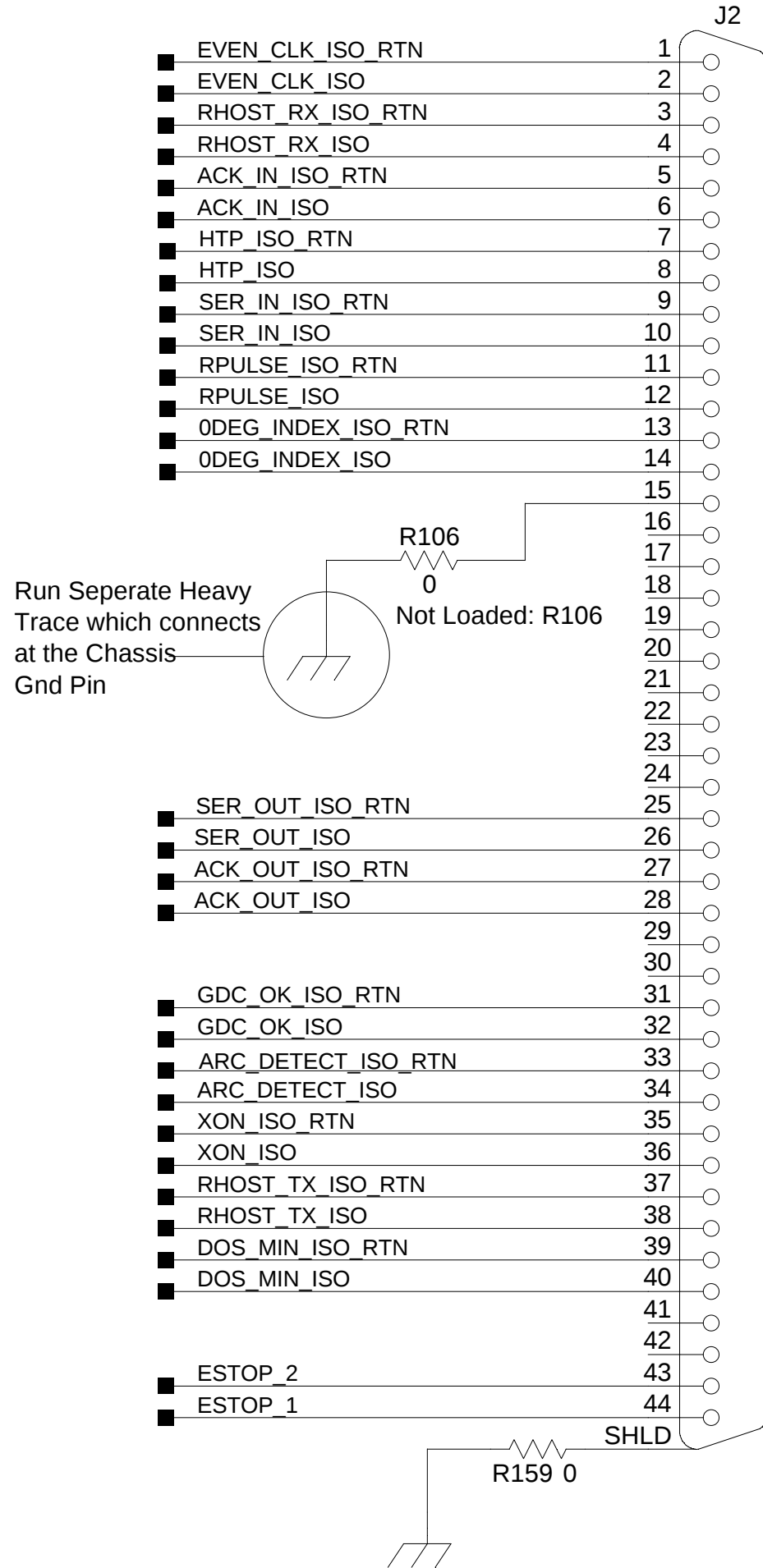
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A

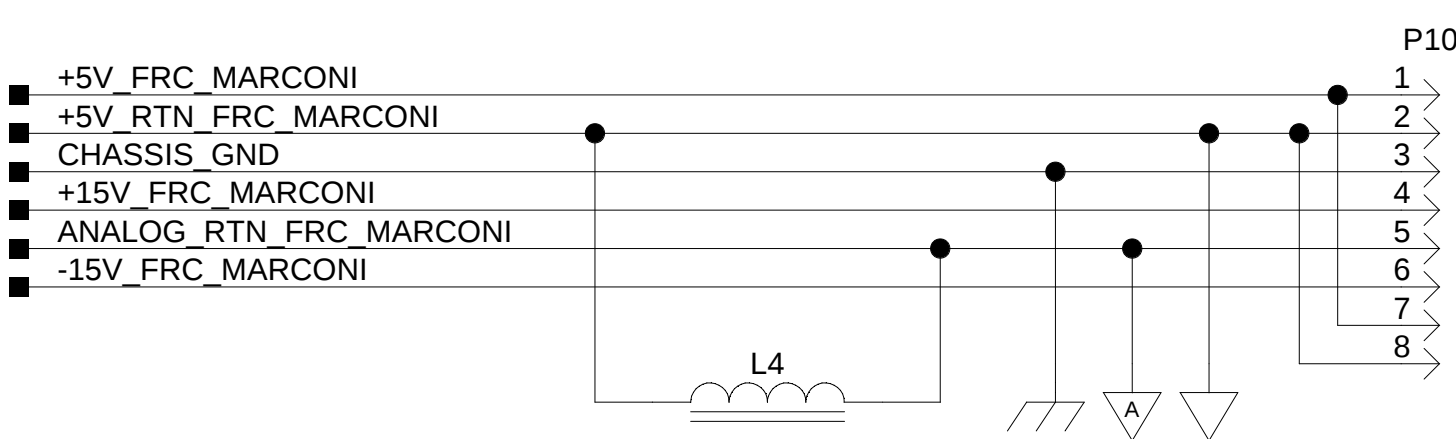
5 VOLT ANALOG SUPPLY



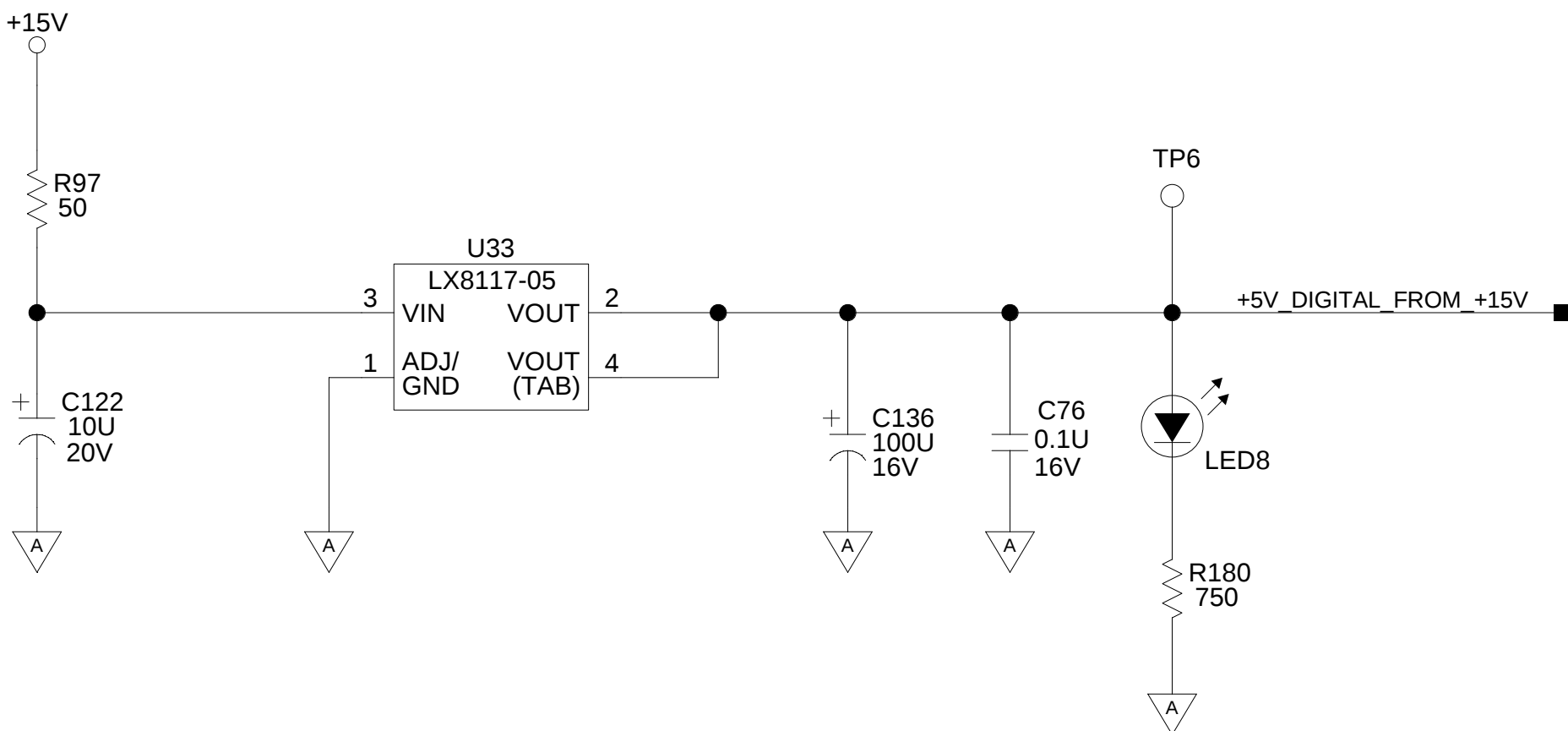
DMC INTERFACE



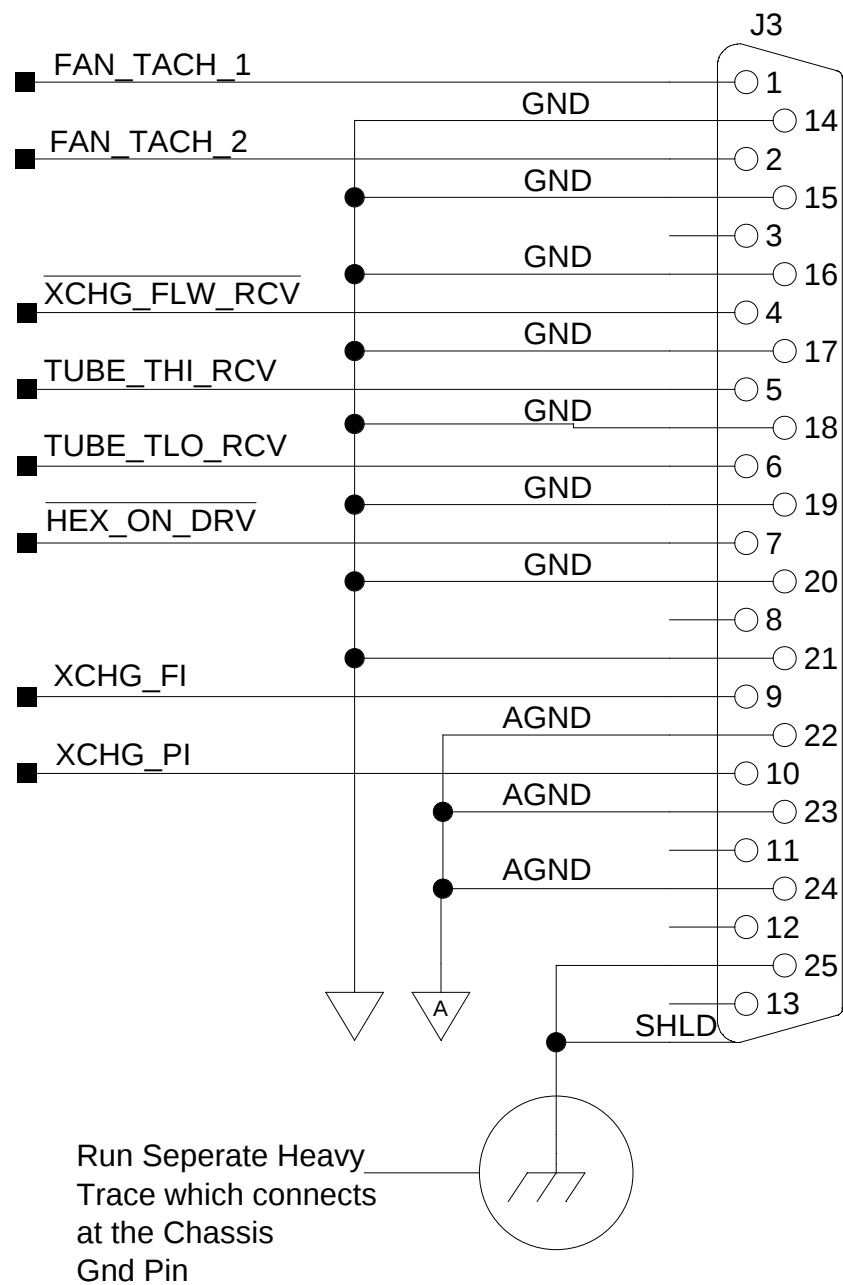
FRC POWER INTERFACE



5 VOLT DIGITAL MIXED MODE SUPPLY



FRC SIGNAL INTERFACE

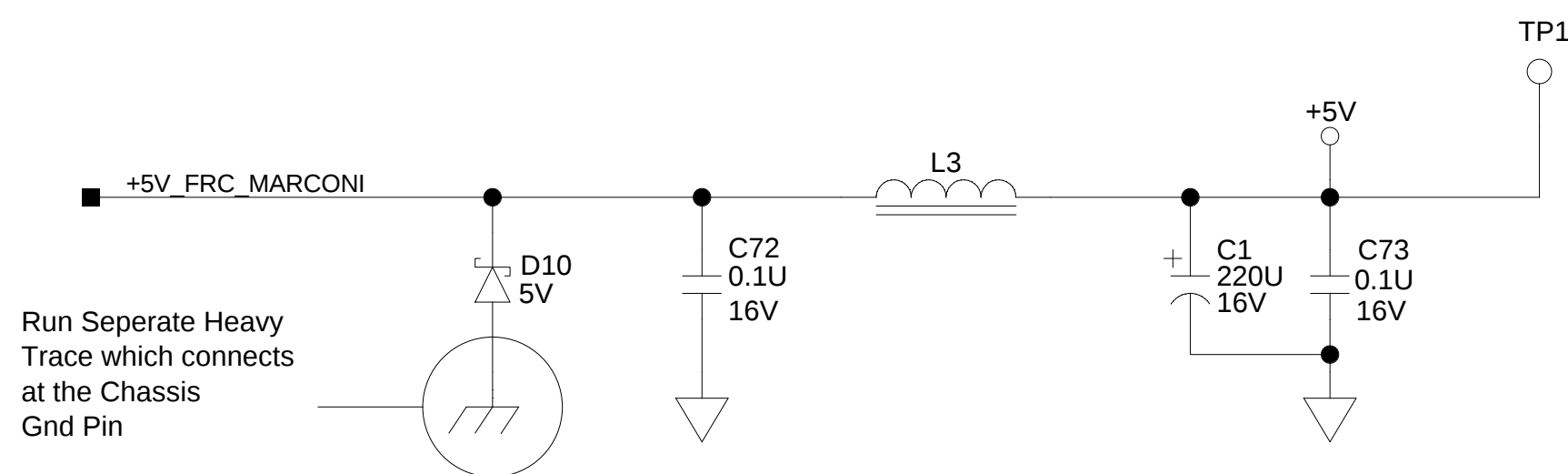


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DWG SCALE	NONE			
DO NOT SCALE				
SHEET 4 OF 20		REV		

D

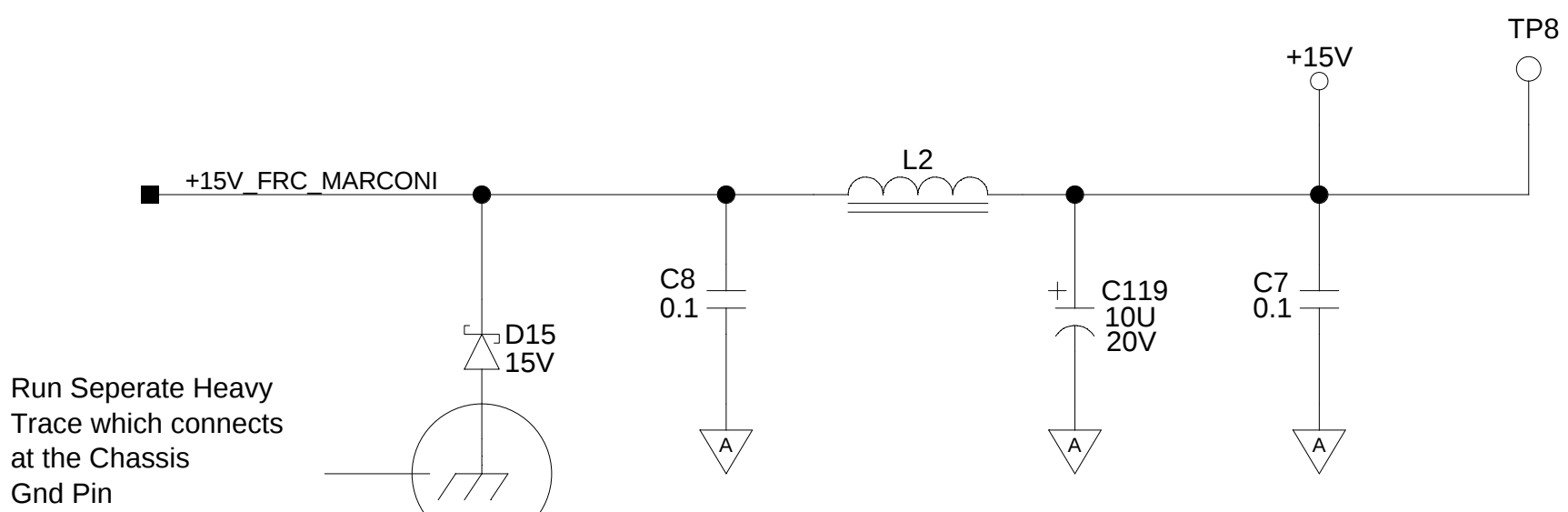
5 VOLT DIGITAL INPUT FILTER



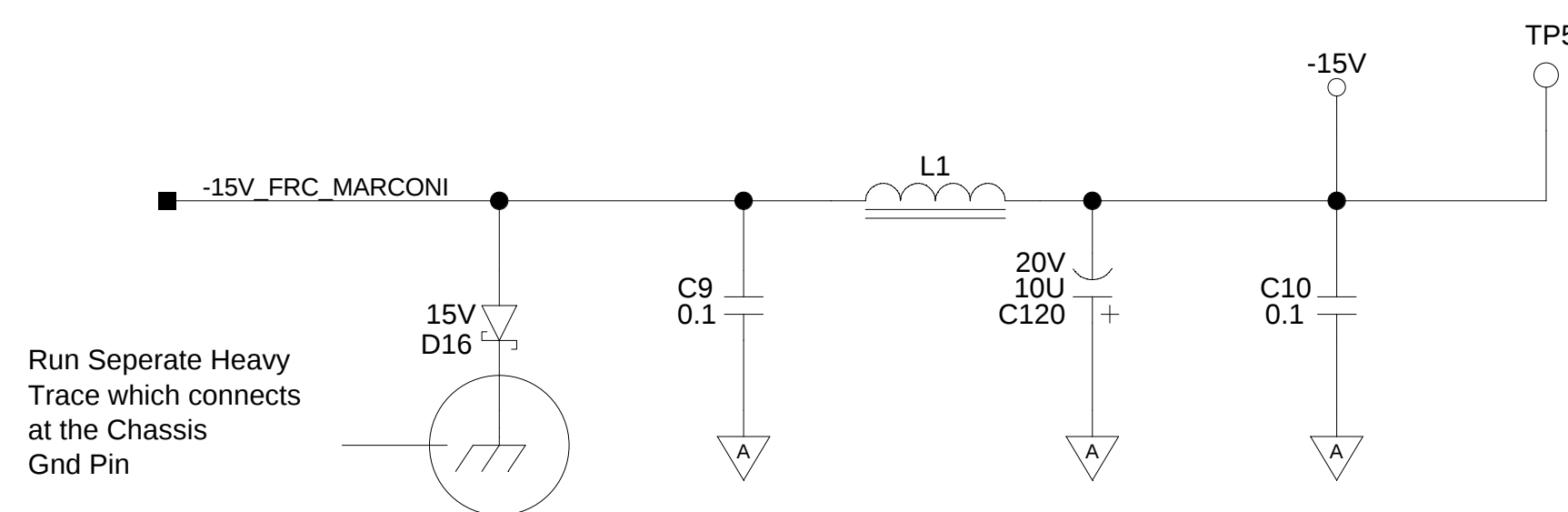
Run Seperate Heavy Trace which connects at the Chassis Gnd Pin

C

+/- 15 VOLT ANALOG INPUT FILTERS



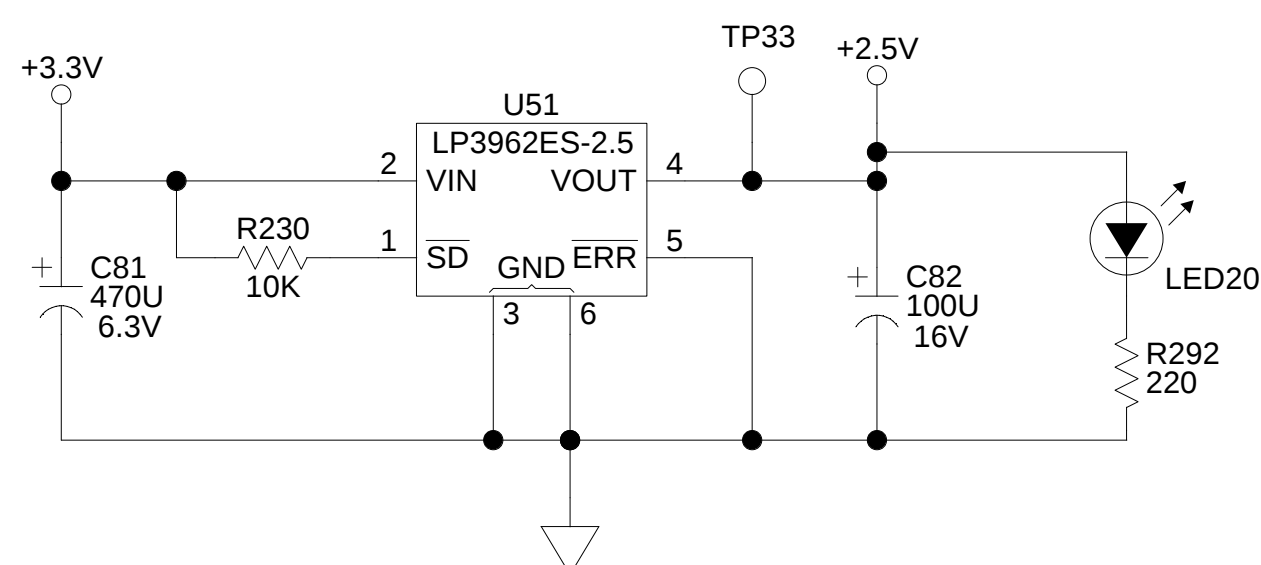
Run Seperate Heavy Trace which connects at the Chassis Gnd Pin



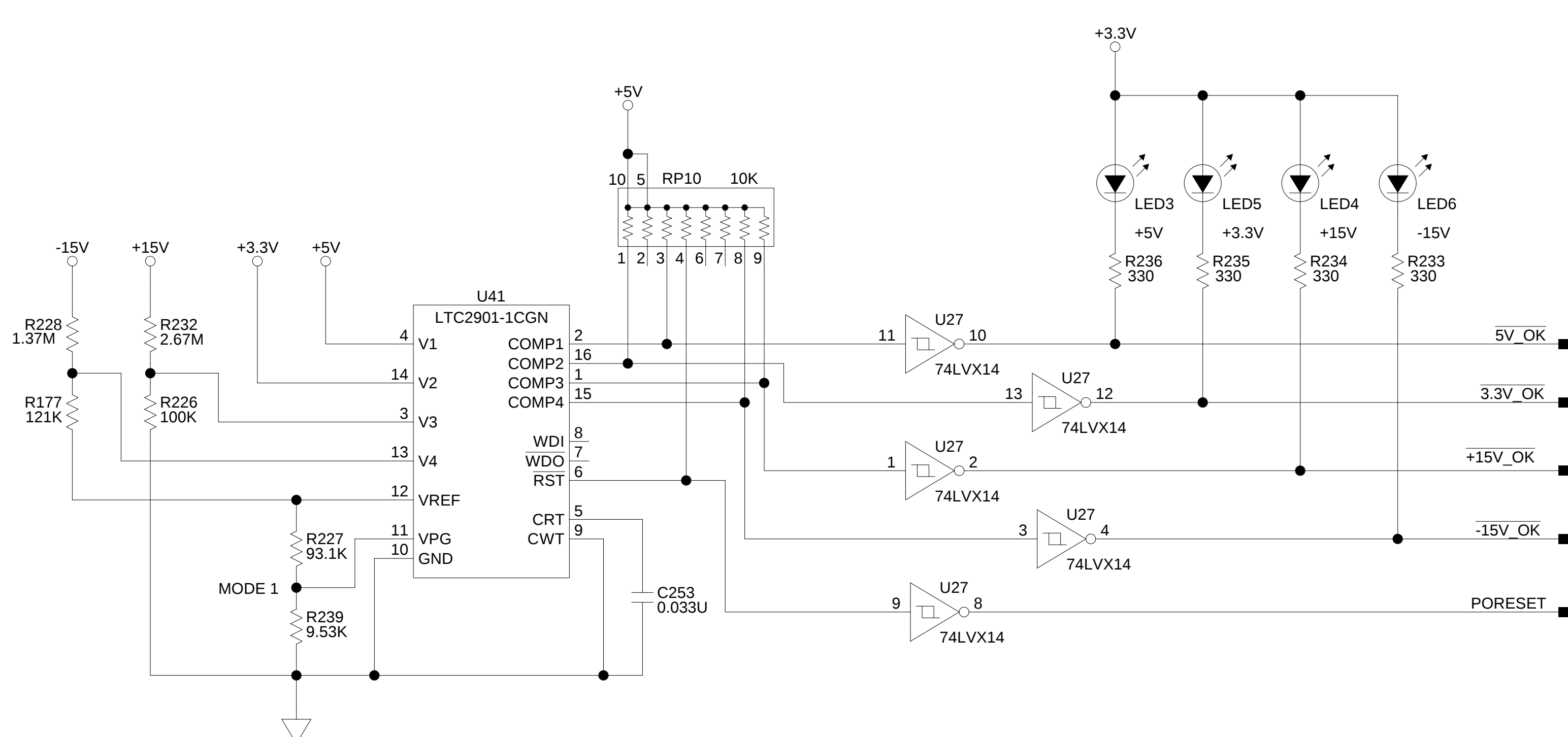
Run Seperate Heavy Trace which connects at the Chassis Gnd Pin

A

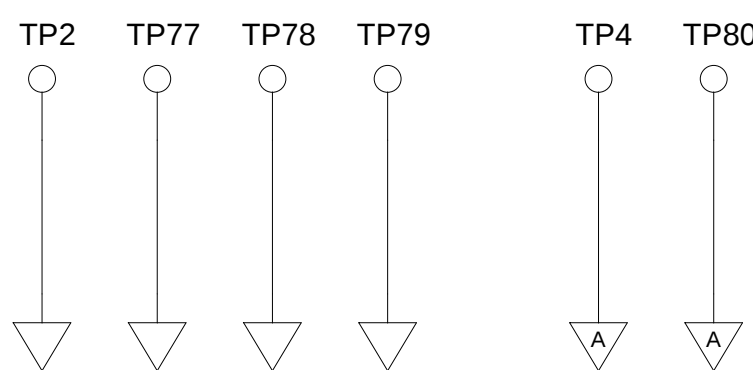
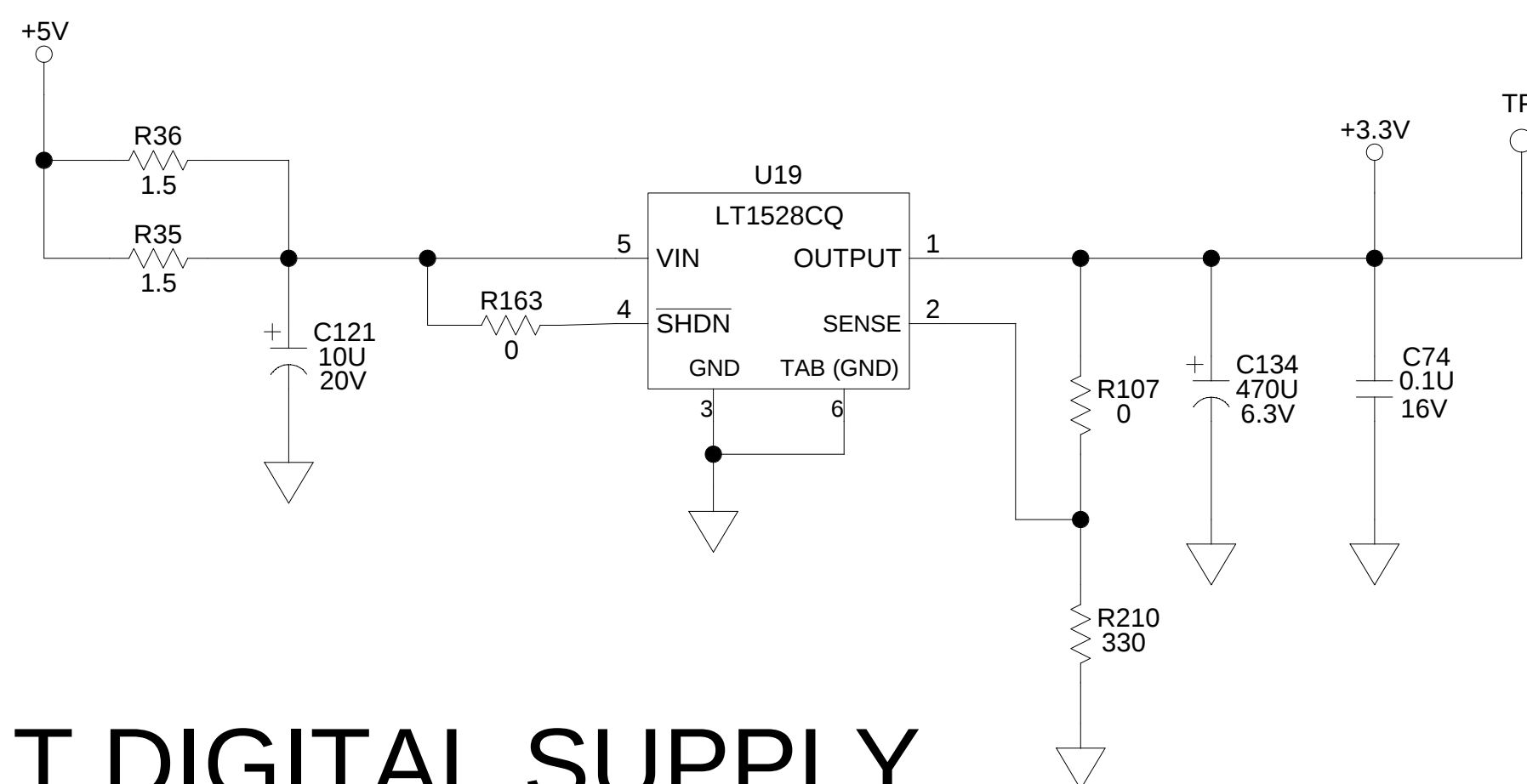
2.5 VOLT DIGITAL SUPPLY



POWER SUPPLY MONITOR / POWER-ON RESET



3.3 VOLT DIGITAL SUPPLY



D

C

B

A

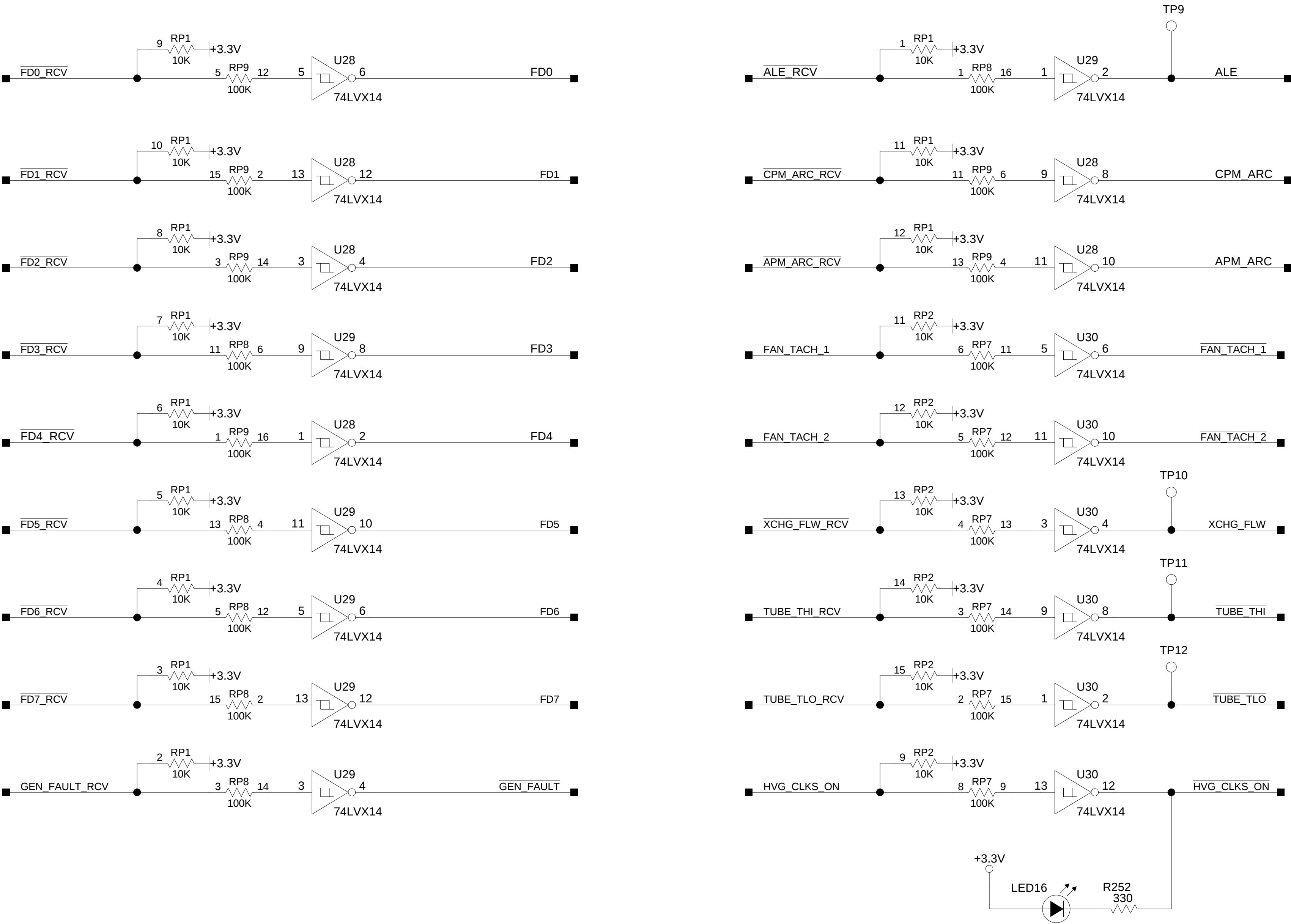
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C

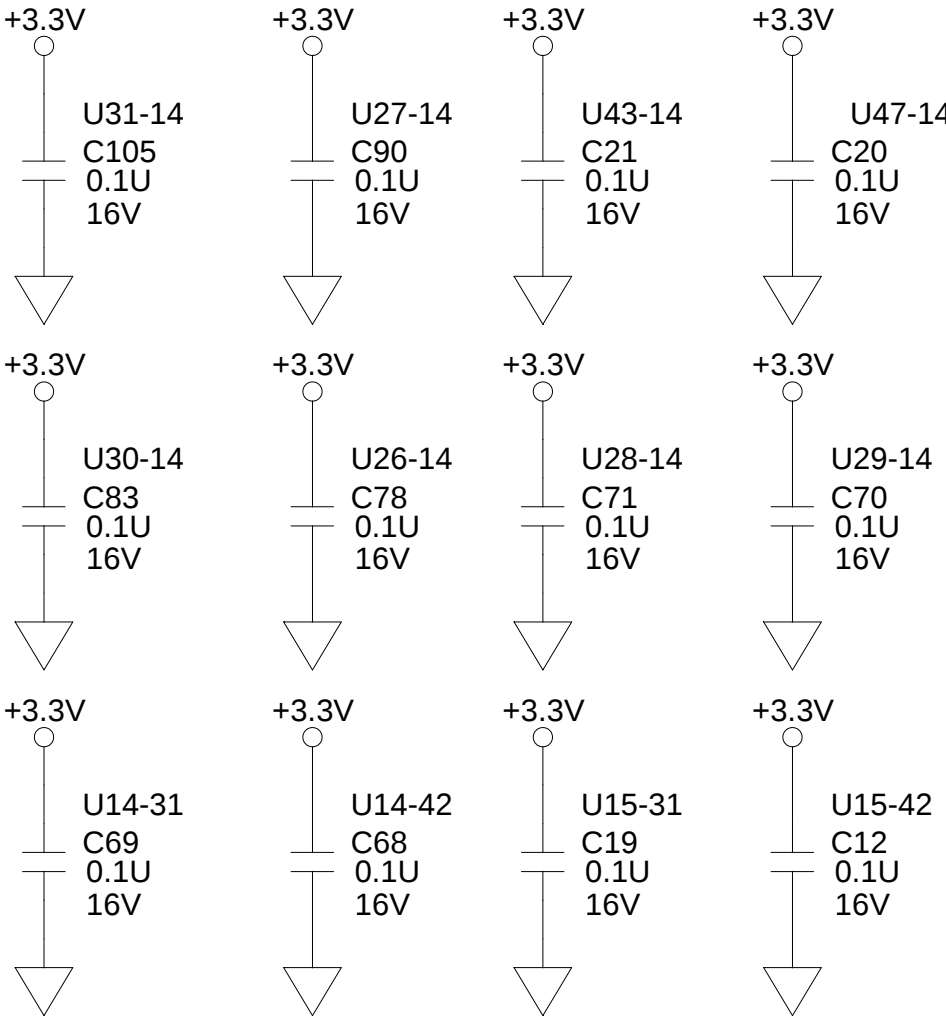
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A

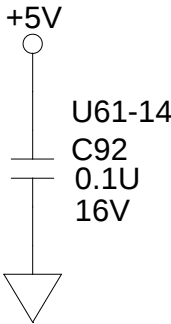
HVG DIGITAL INPUT



+3.3V DECOUPLING



+5V DECOUPLING



D

C

B

A

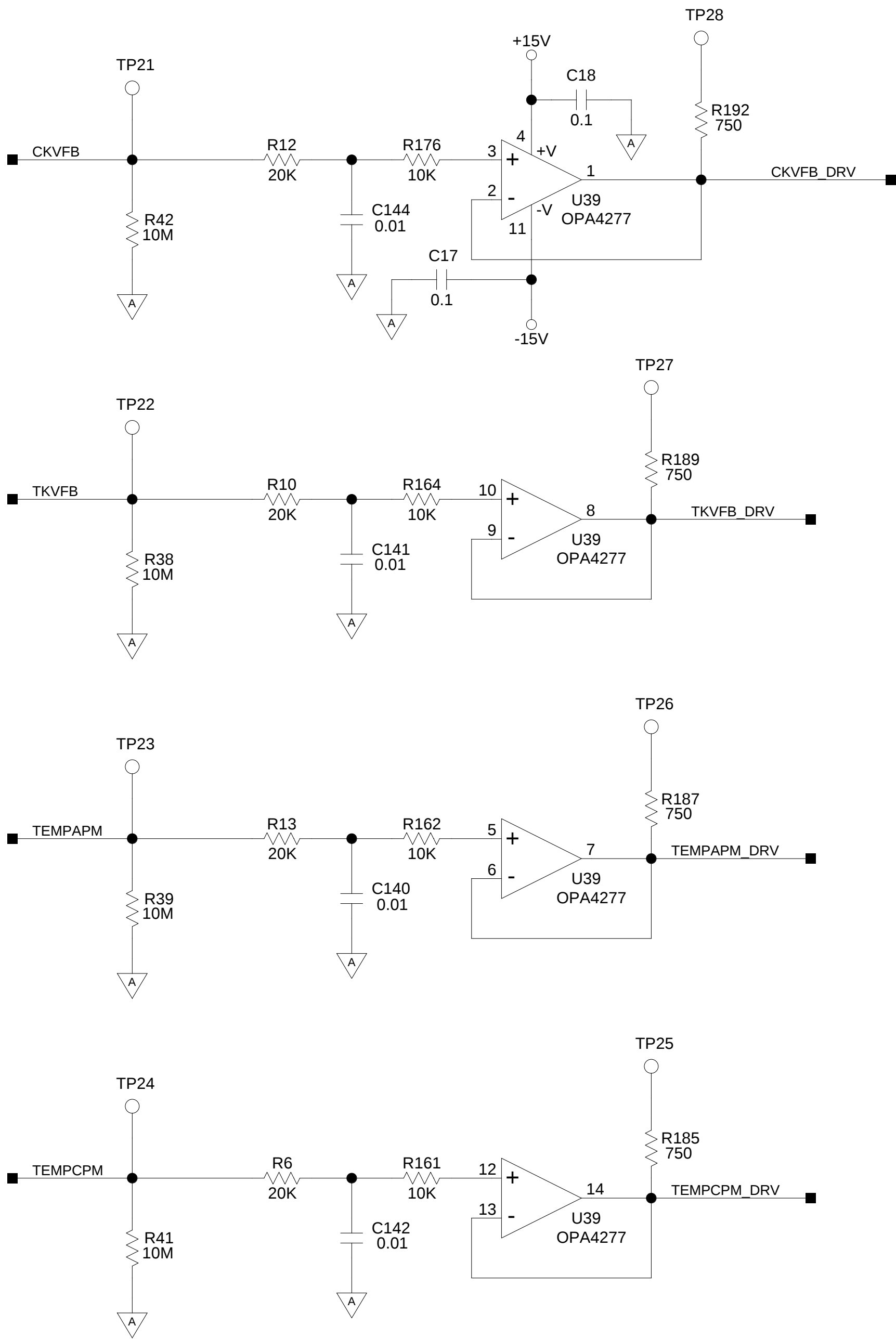
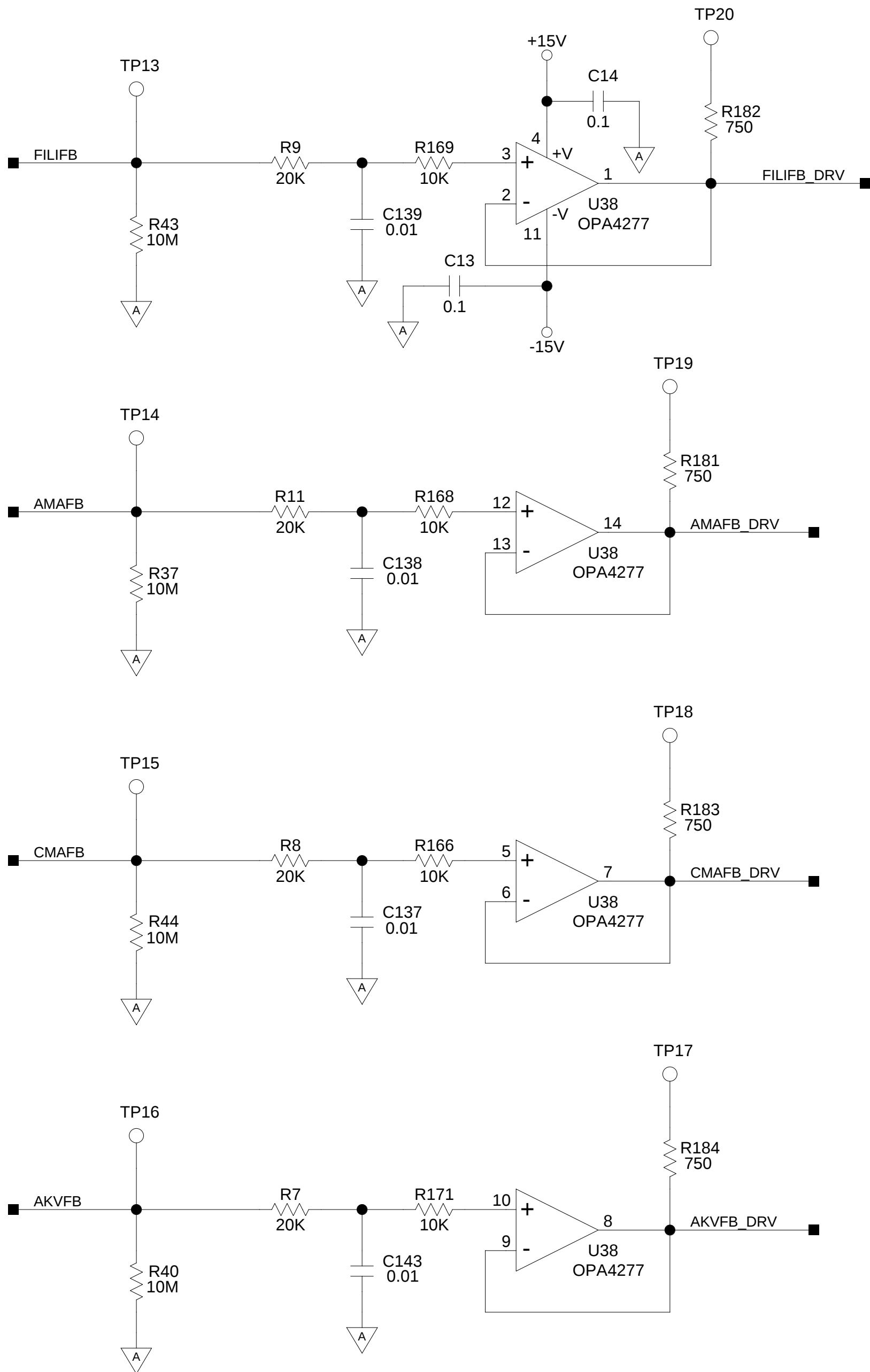
D

C

B

A

HVG ANALOG INPUTS



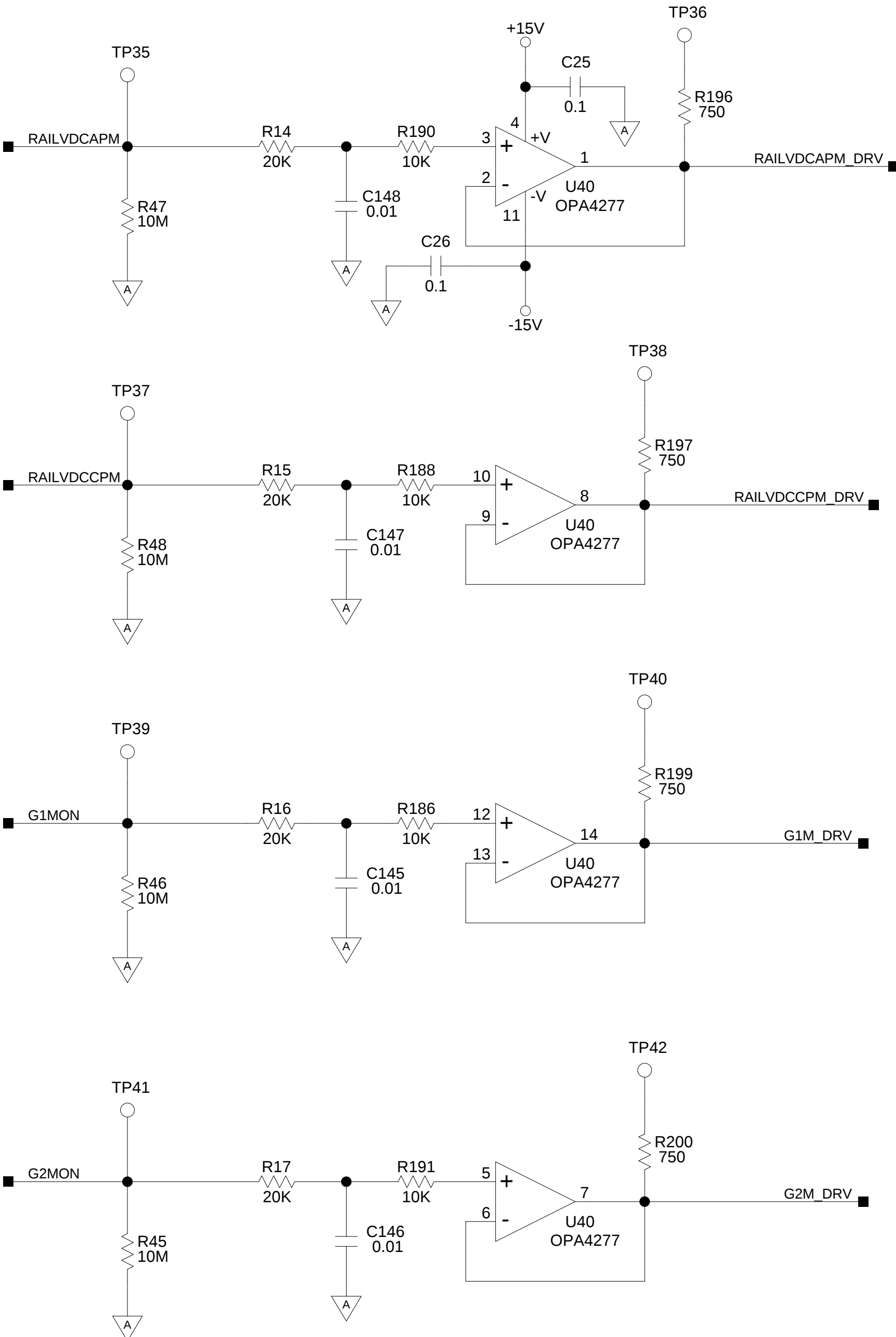
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C

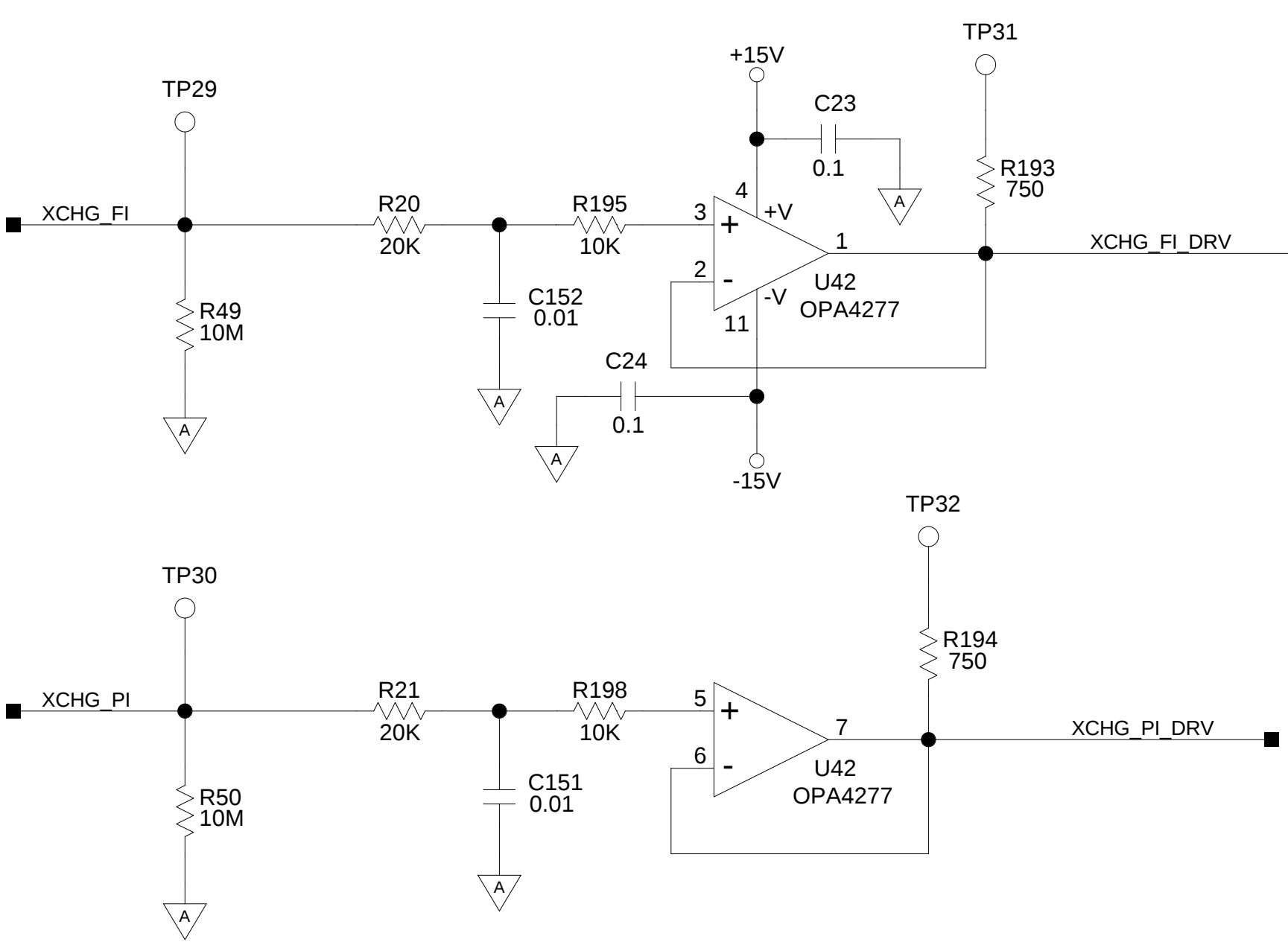
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A

HVG ANALOG INPUTS

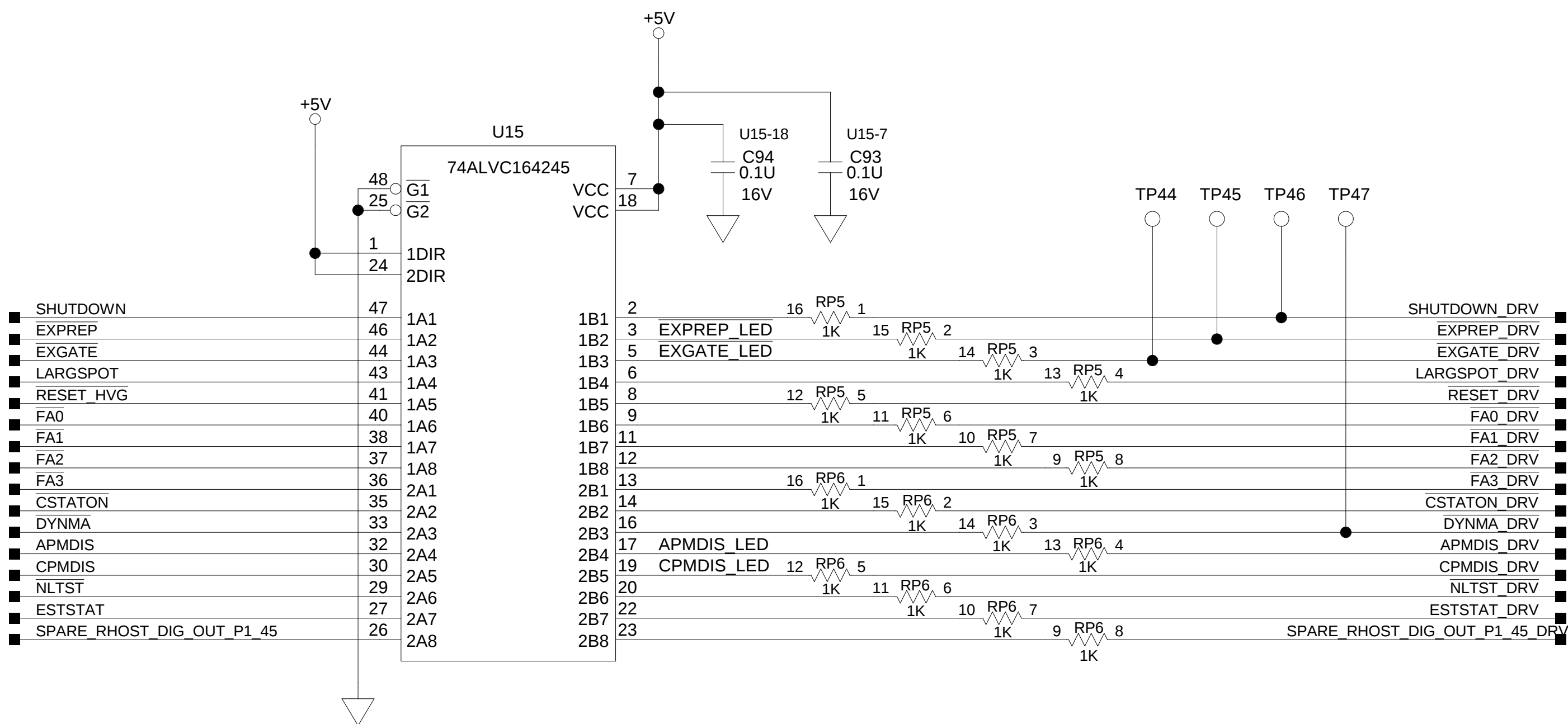


FRC ANALOG INPUTS



D

HIGH VOLTAGE GENERATOR OUTPUTS

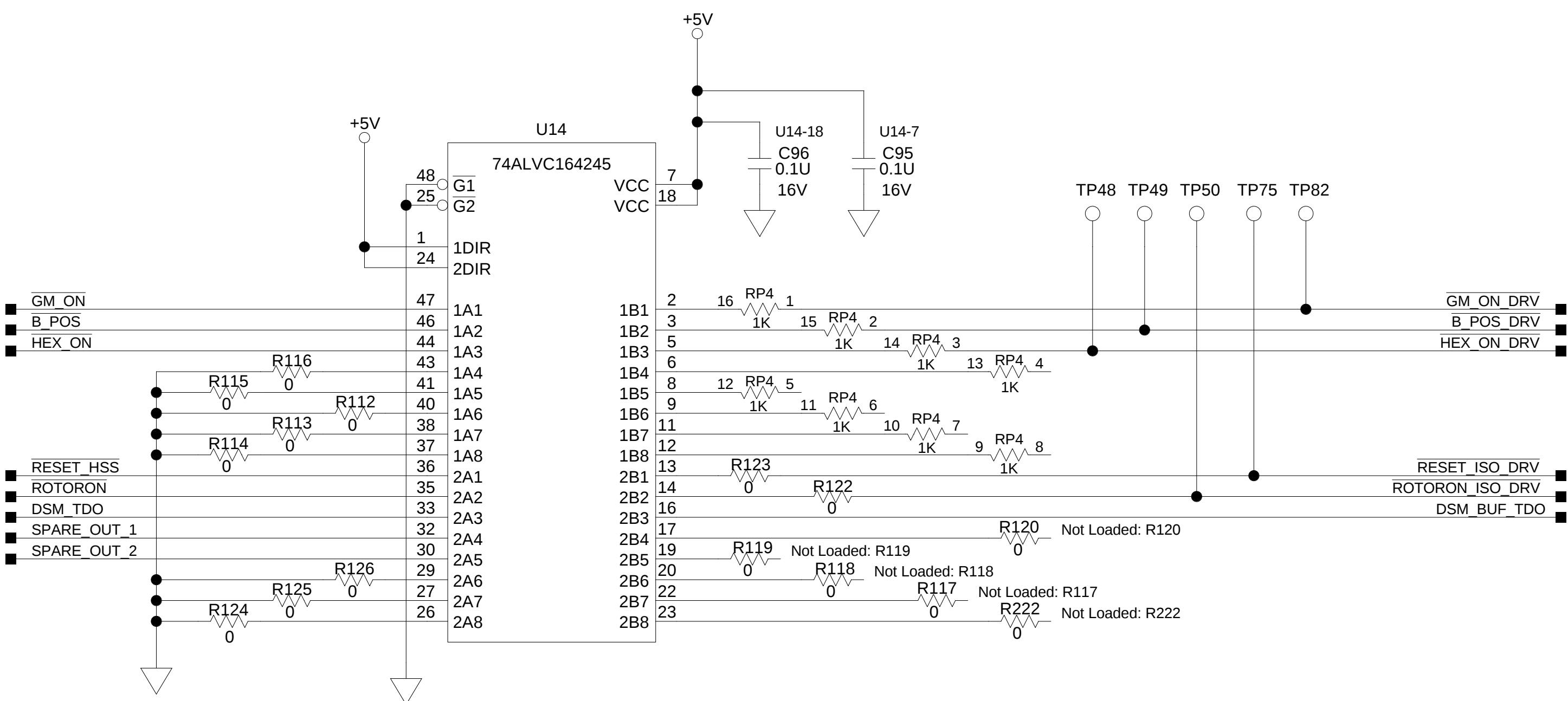


C

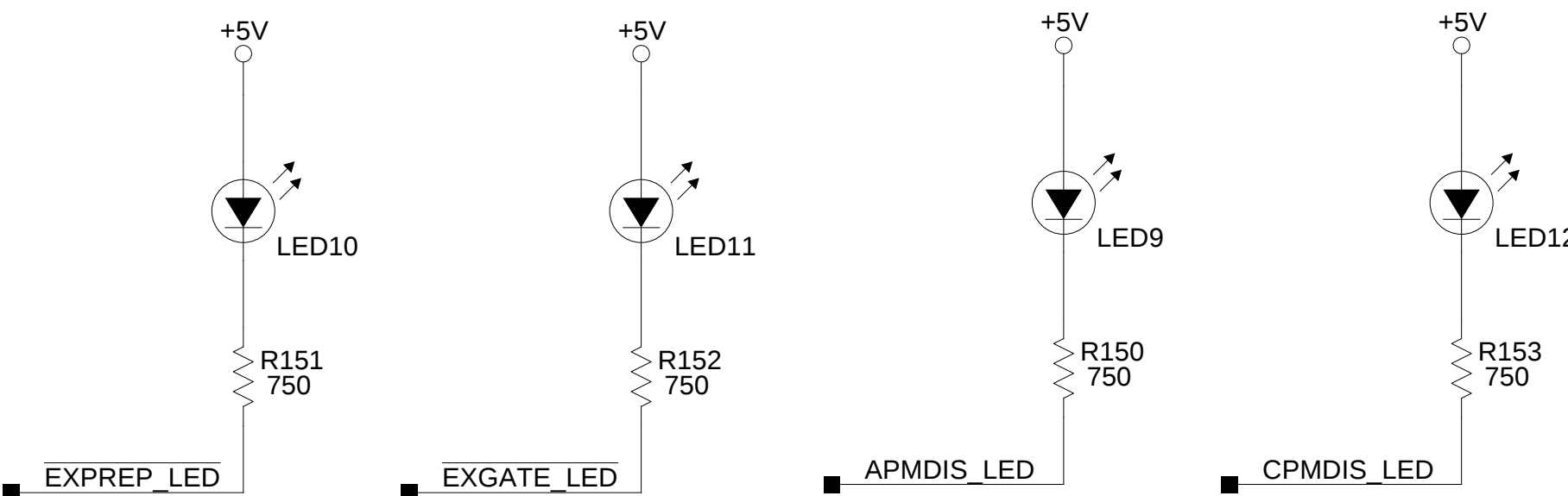
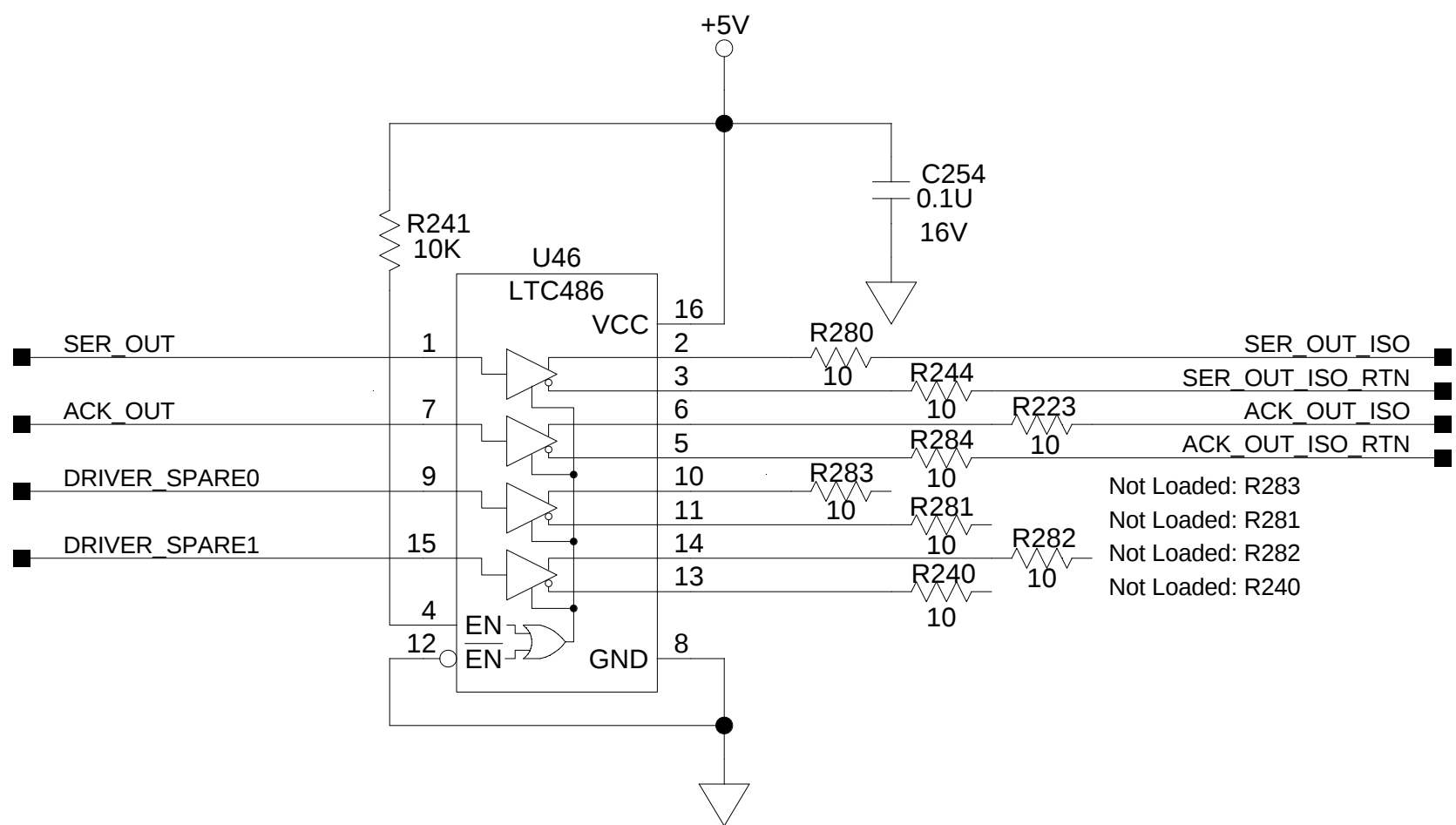
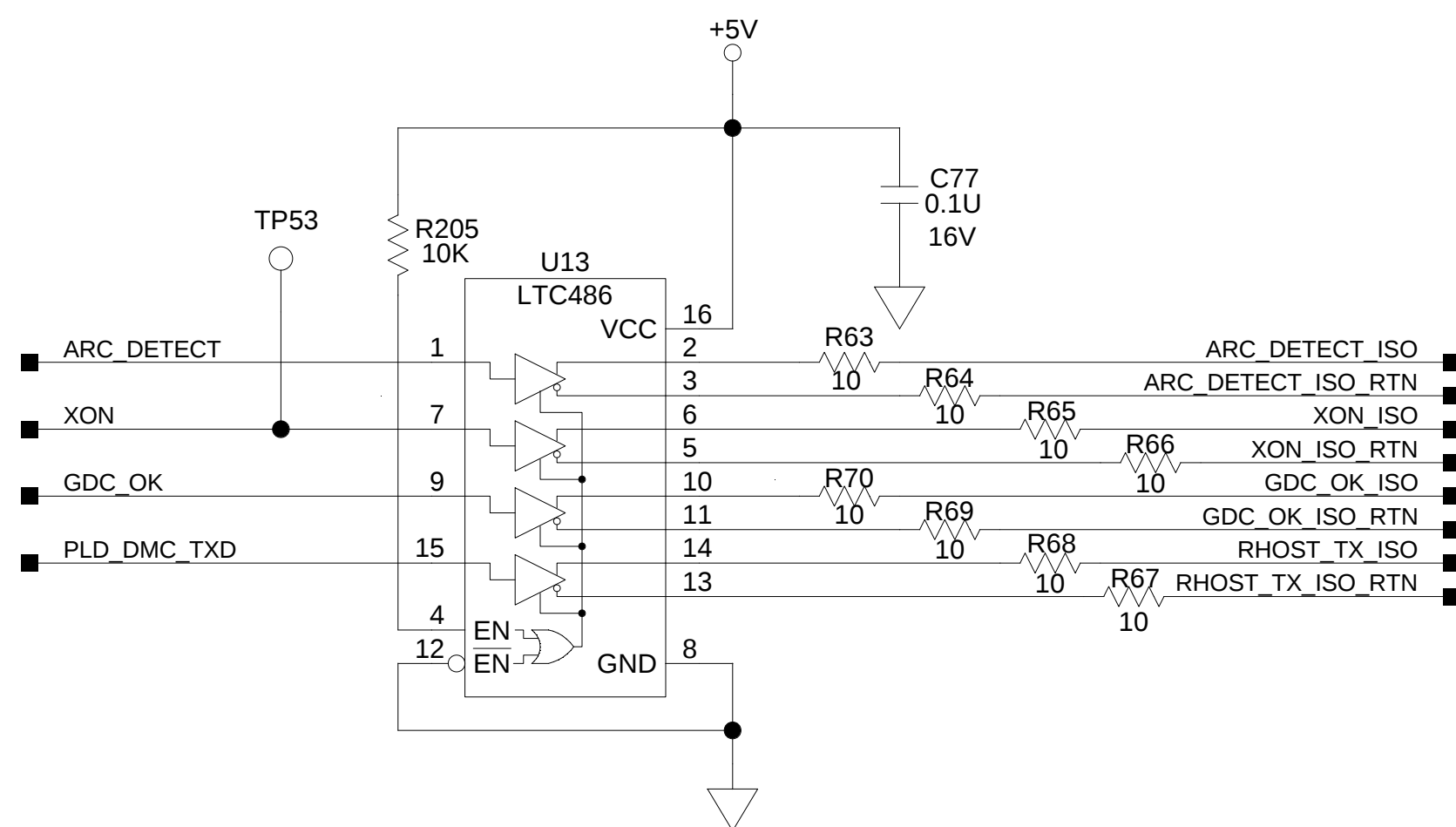
B

A

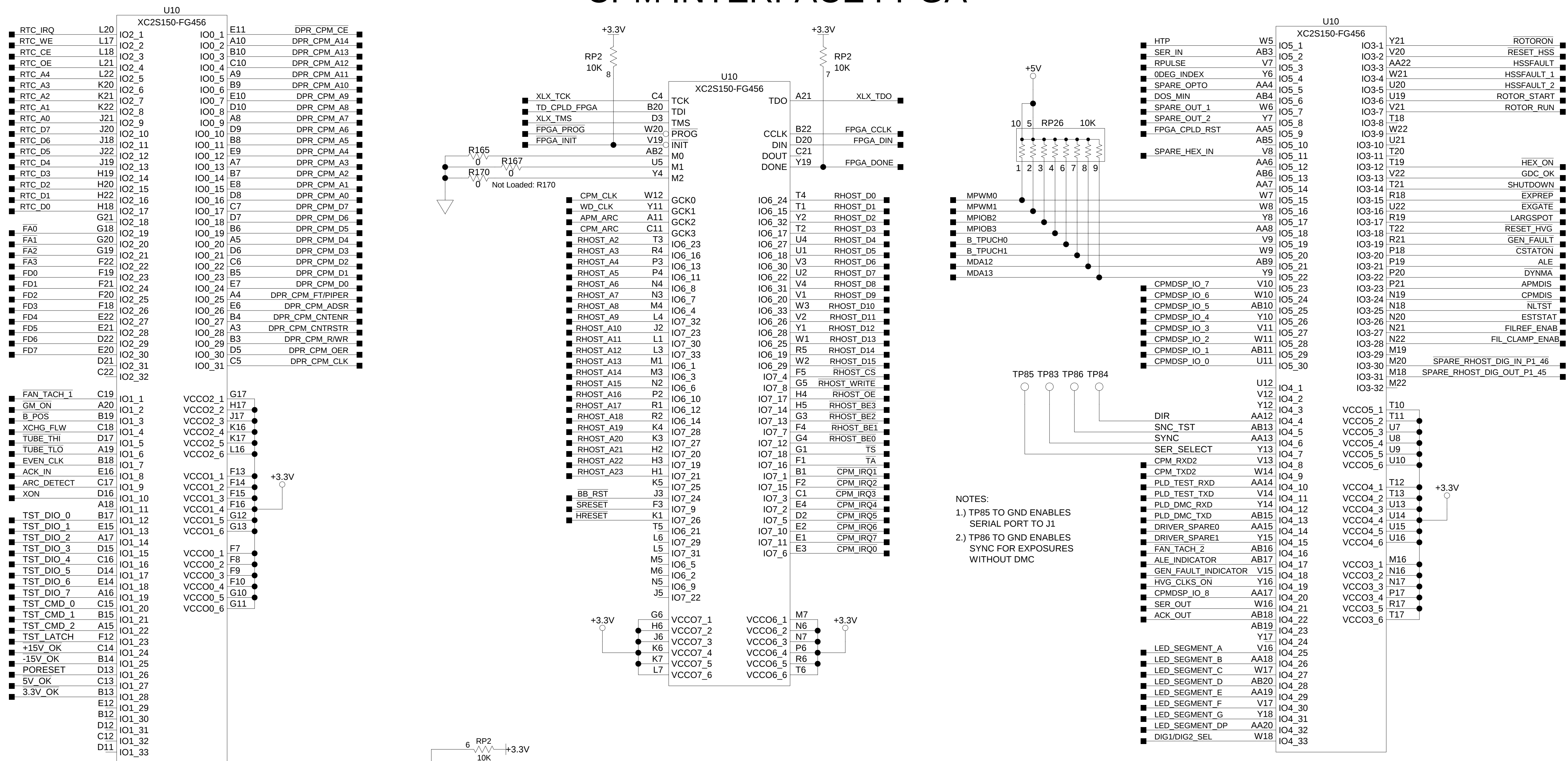
HIGH SPEED STARTER /GRID MODULATOR OUTPUTS



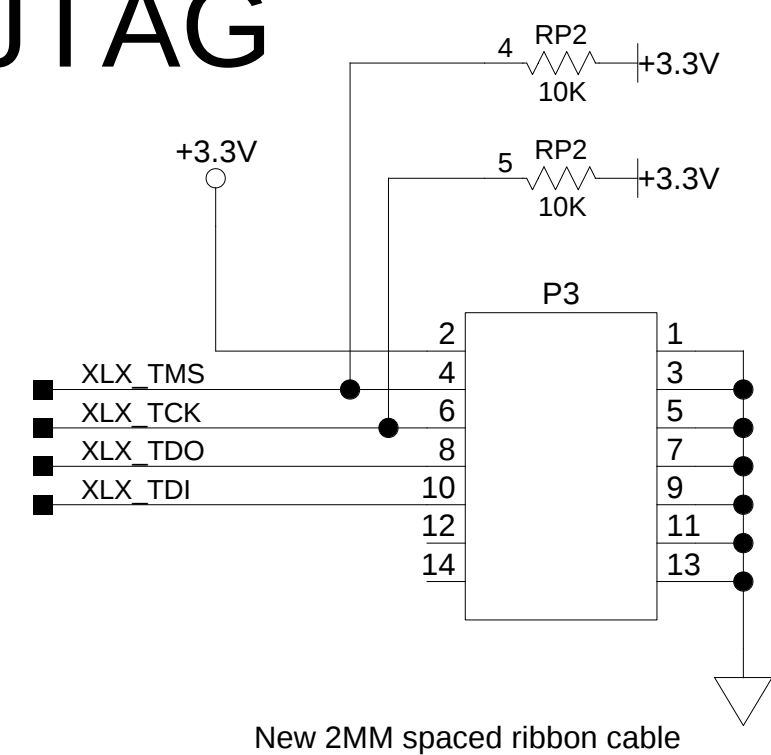
DMC OUTPUTS



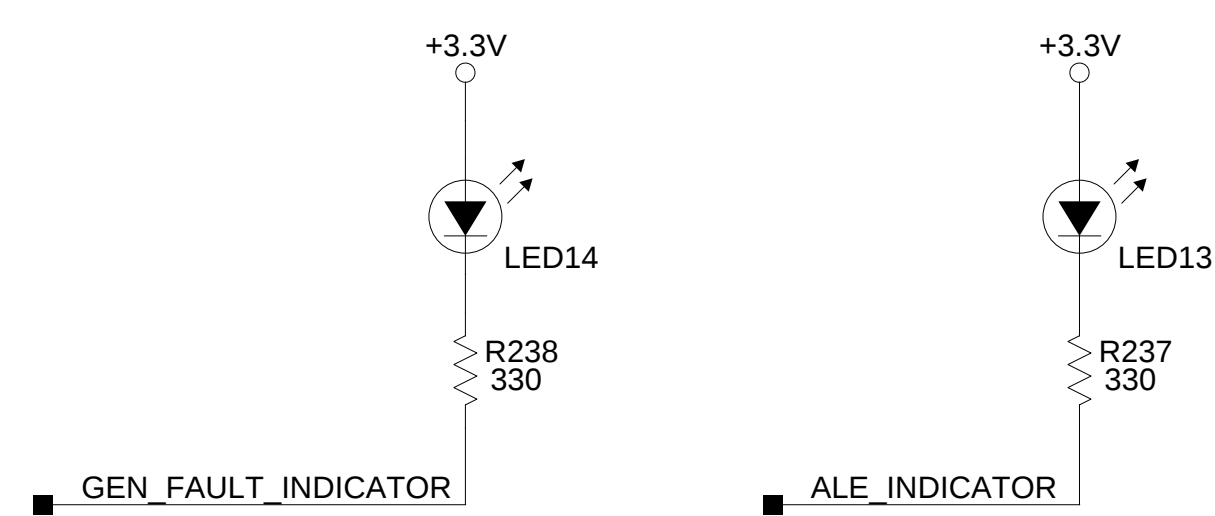
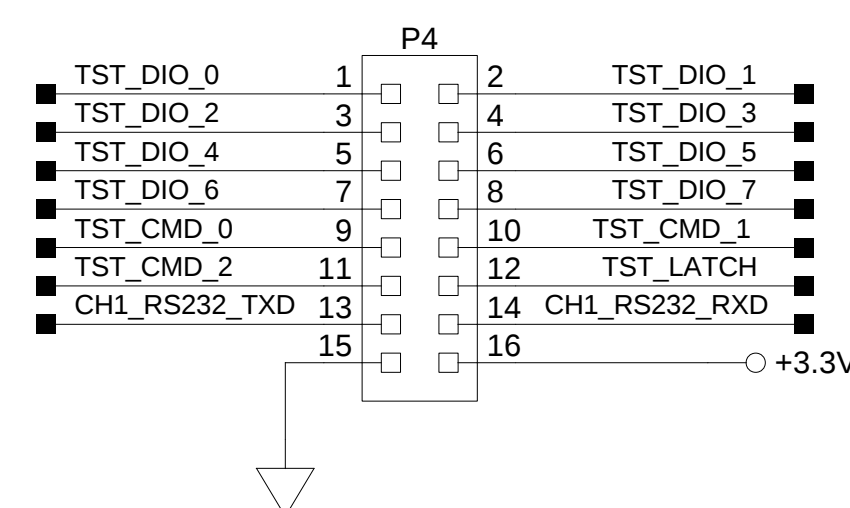
CPM INTERFACE FPGA



XLX JTAG



CPM/FPGA TEST



D

C

B

A

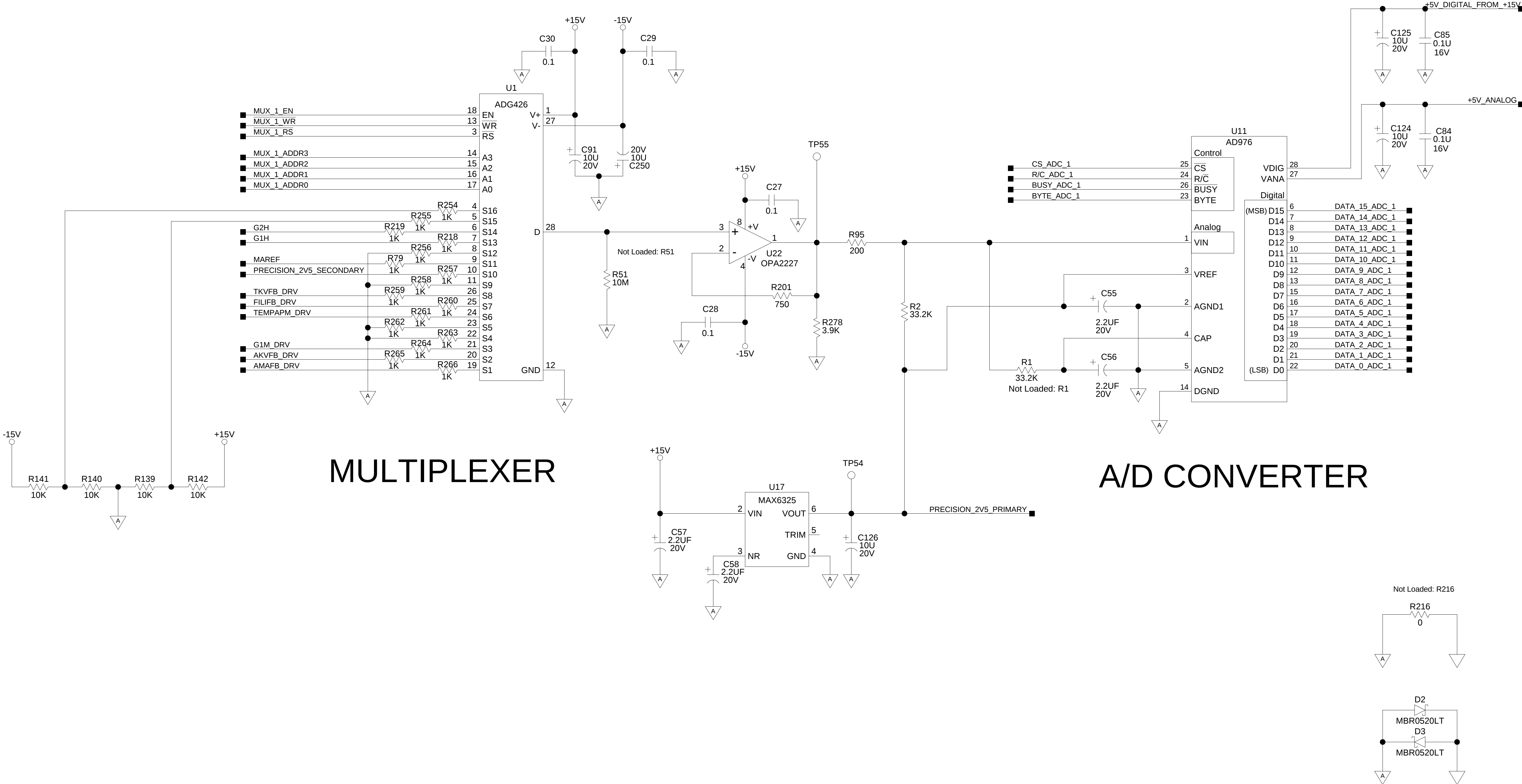
D

C

B

A

MAIN ANALOG-TO-DIGITAL CONVERSION



MULTIPLEXER

A/D CONVERTER

D

C

B

A

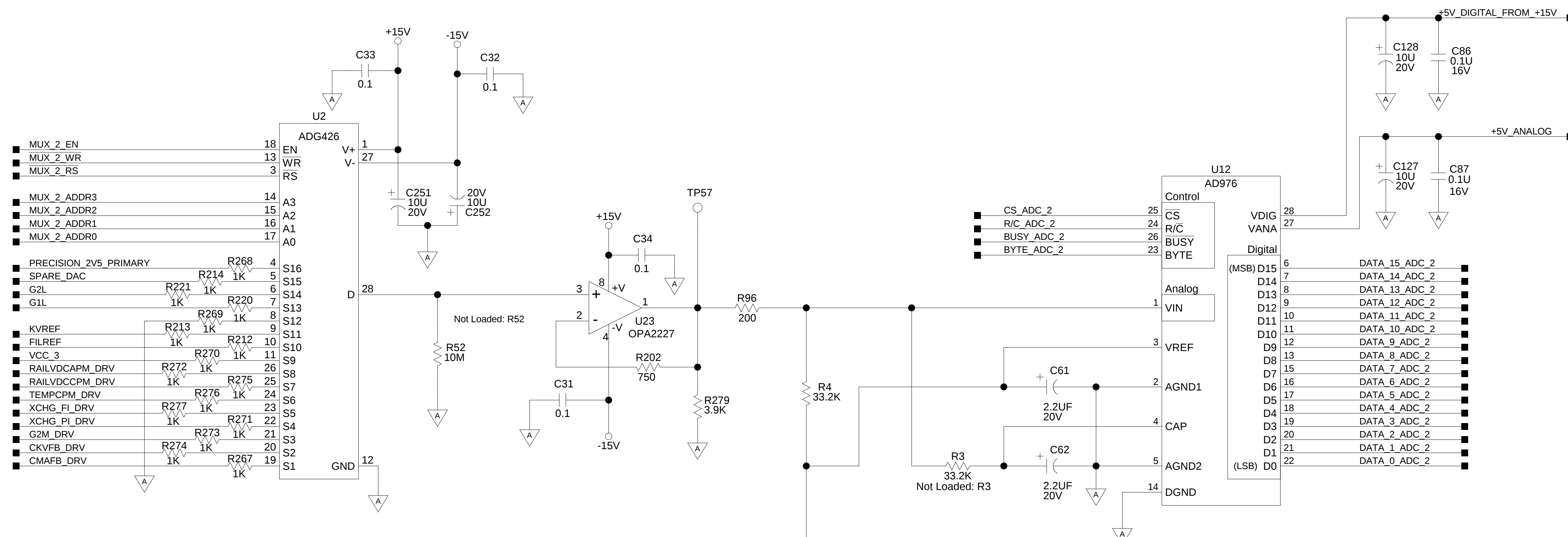
D

C

B

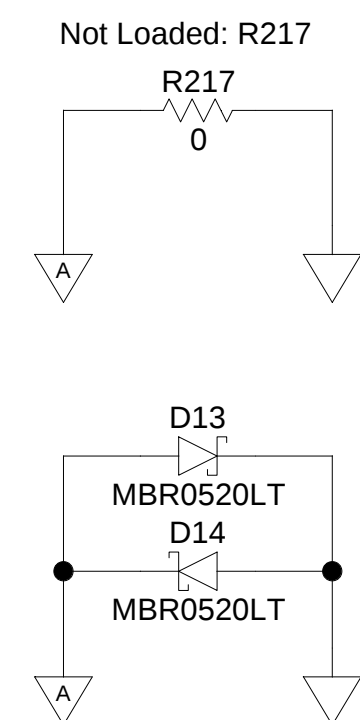
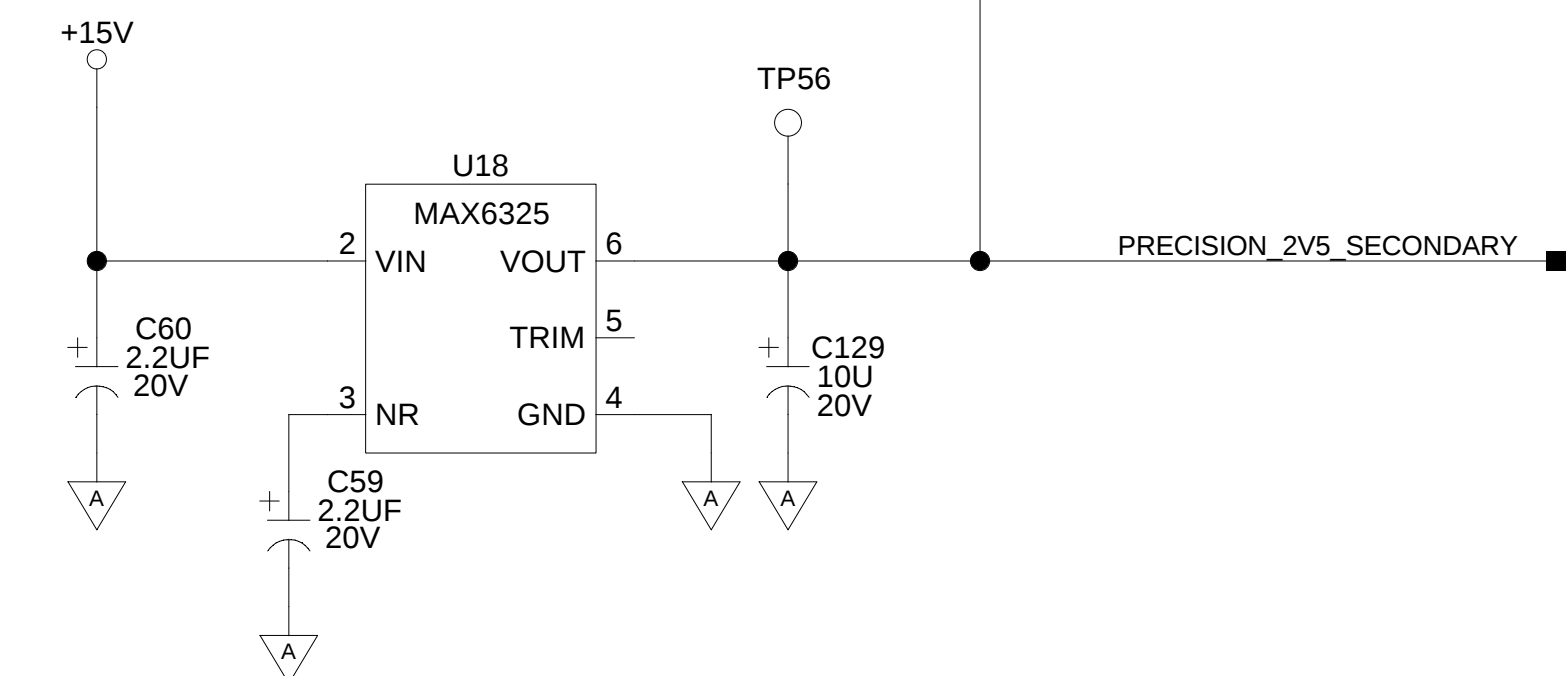
A

SECONDARY ANALOG-TO-DIGITAL CONVERSION



MULTIPLEXER

A/D CONVERTER



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DWG SCALE	NONE			
DO NOT SCALE			SHEET 12 OF 20	REV

2 2-10-2004_9:18

1 453567005941_REV_B 12

DIGITAL-TO-ANALOG CONVERSION

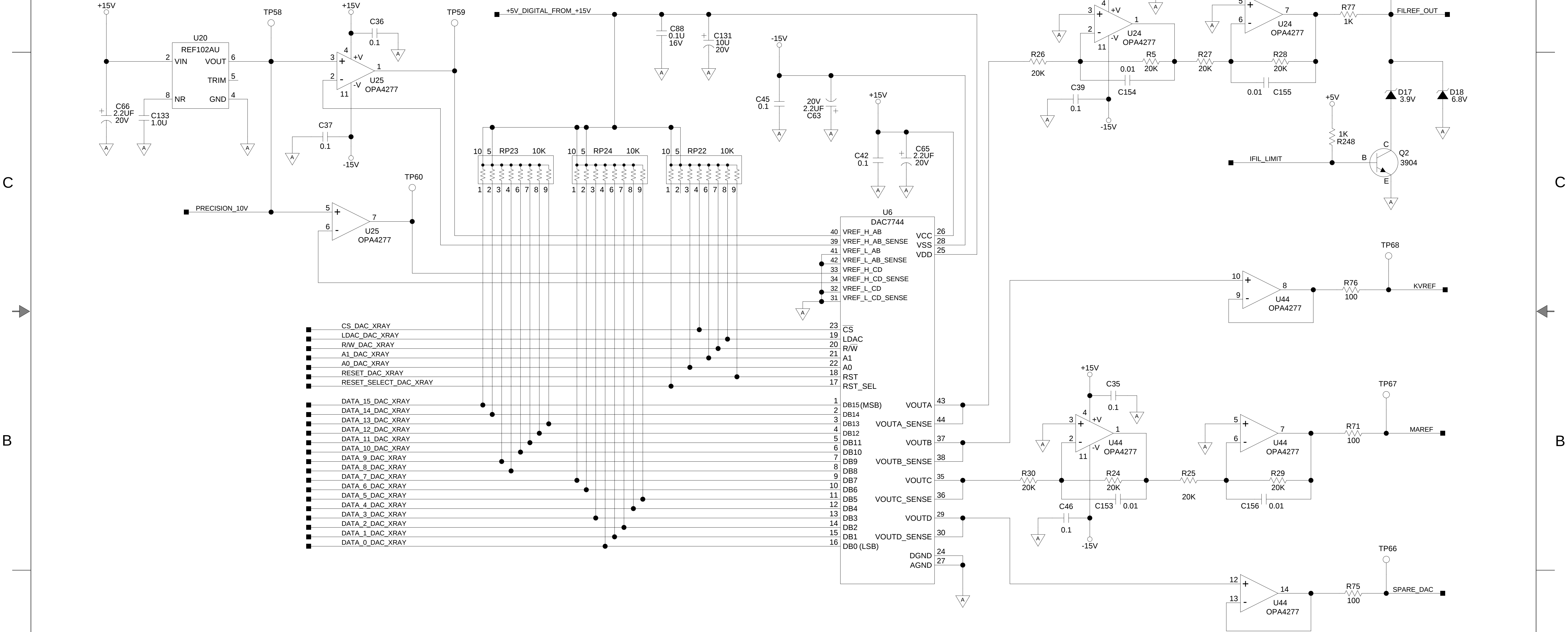
D

C

B

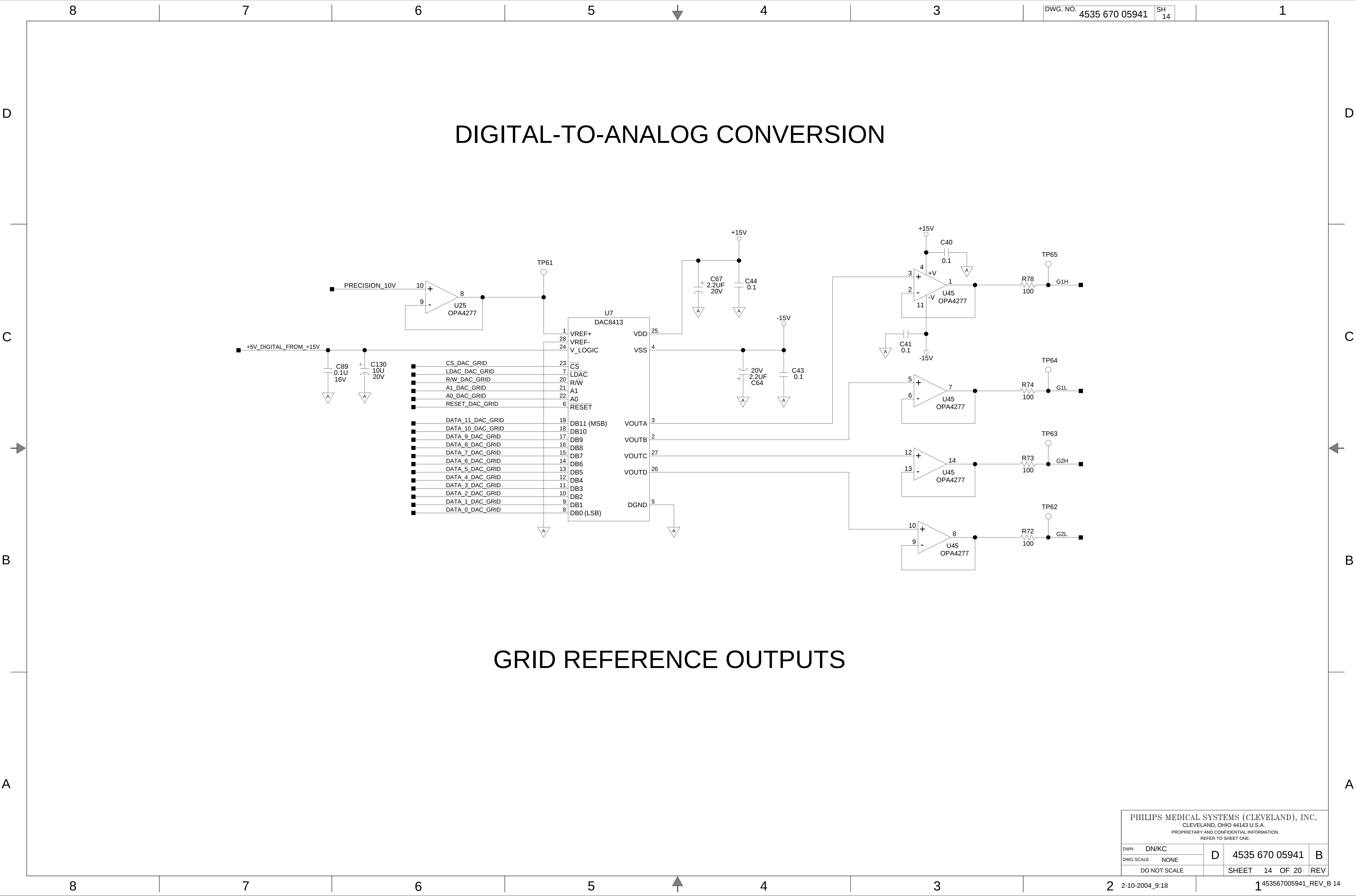
A

X-RAY REFERENCE OUTPUTS



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DWG SCALE	NONE			
DO NOT SCALE				



D

C

B

A

D

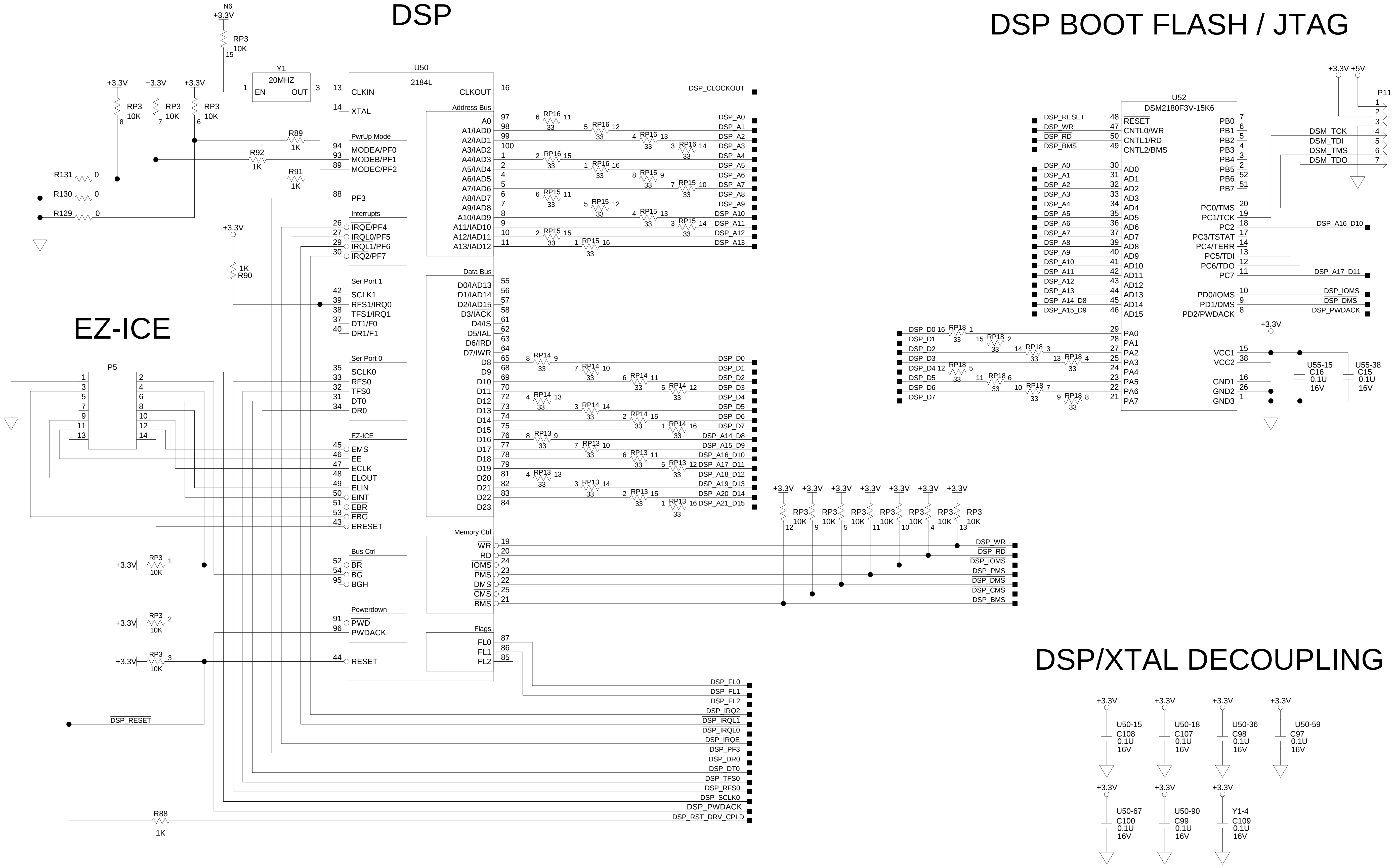
C

B

A

DSP

DSP BOOT FLASH / JTAG



D

C

B

A

D

C

B

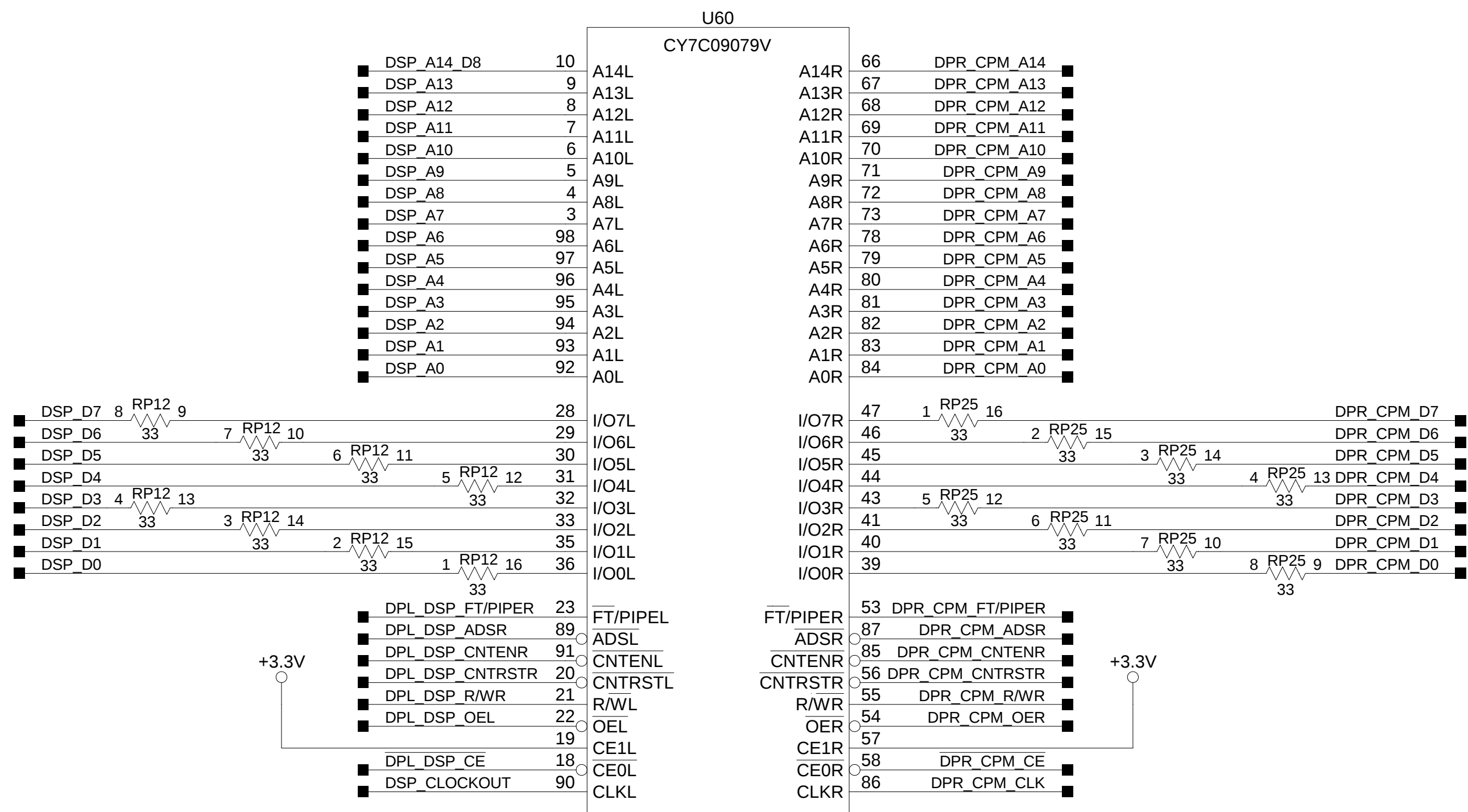
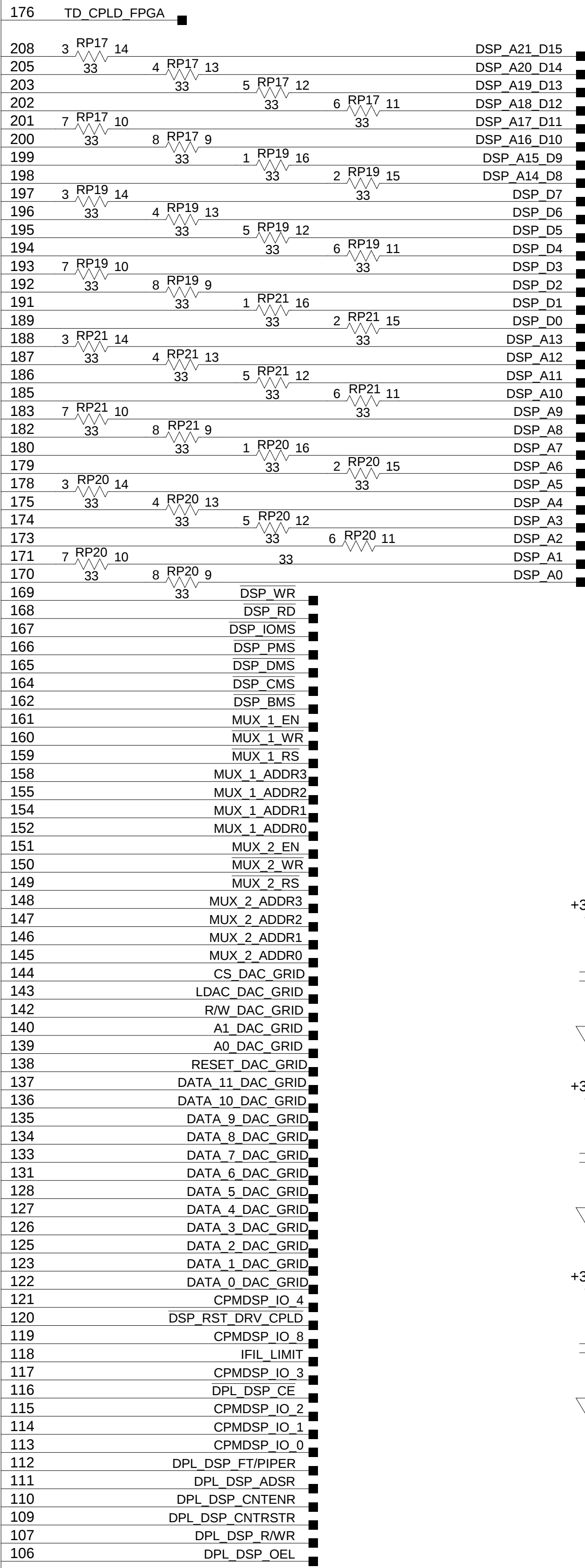
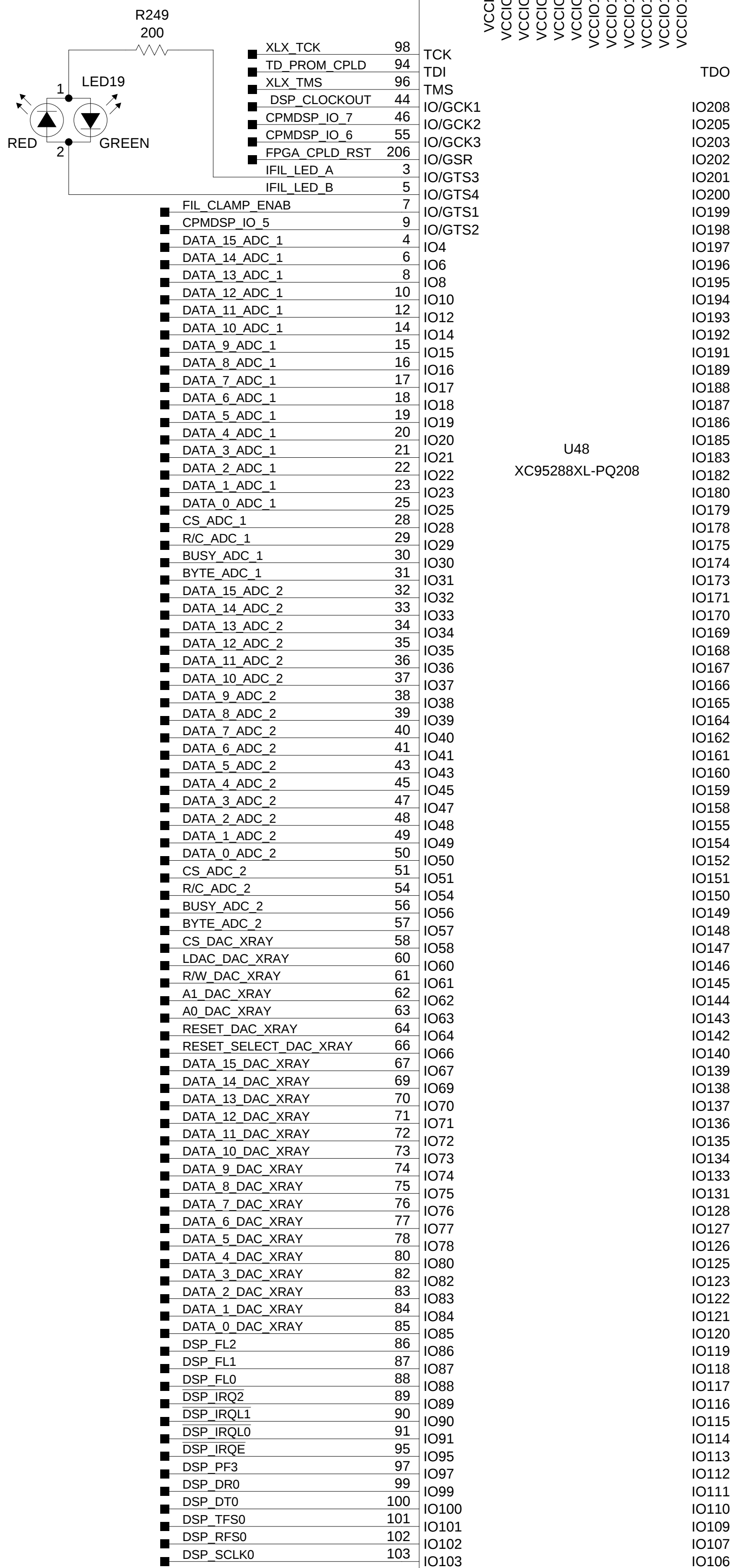
A

DSP INTERFACE CPLD

DUAL PORT RAM MEMORY

CPLD DECOUPLING

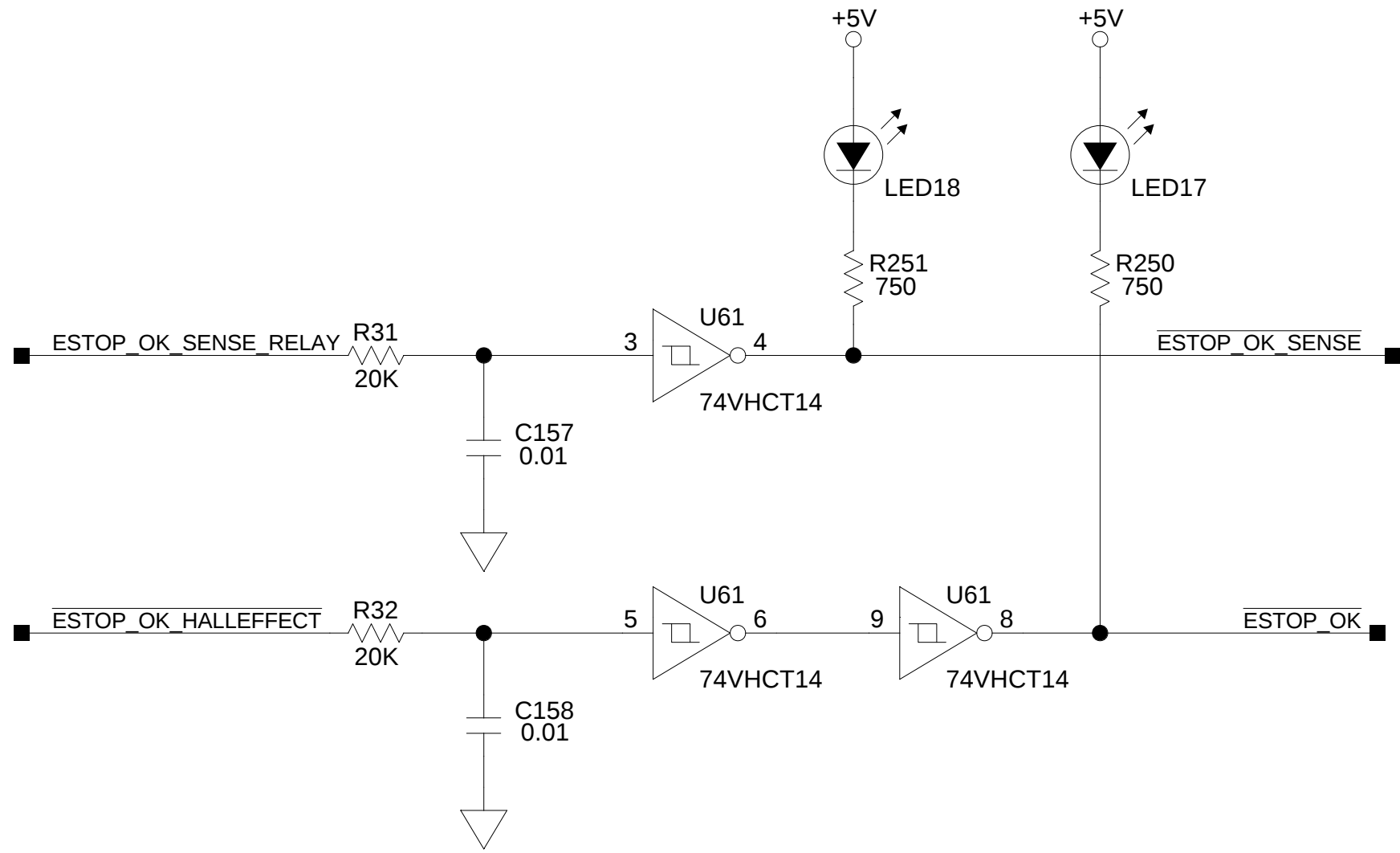
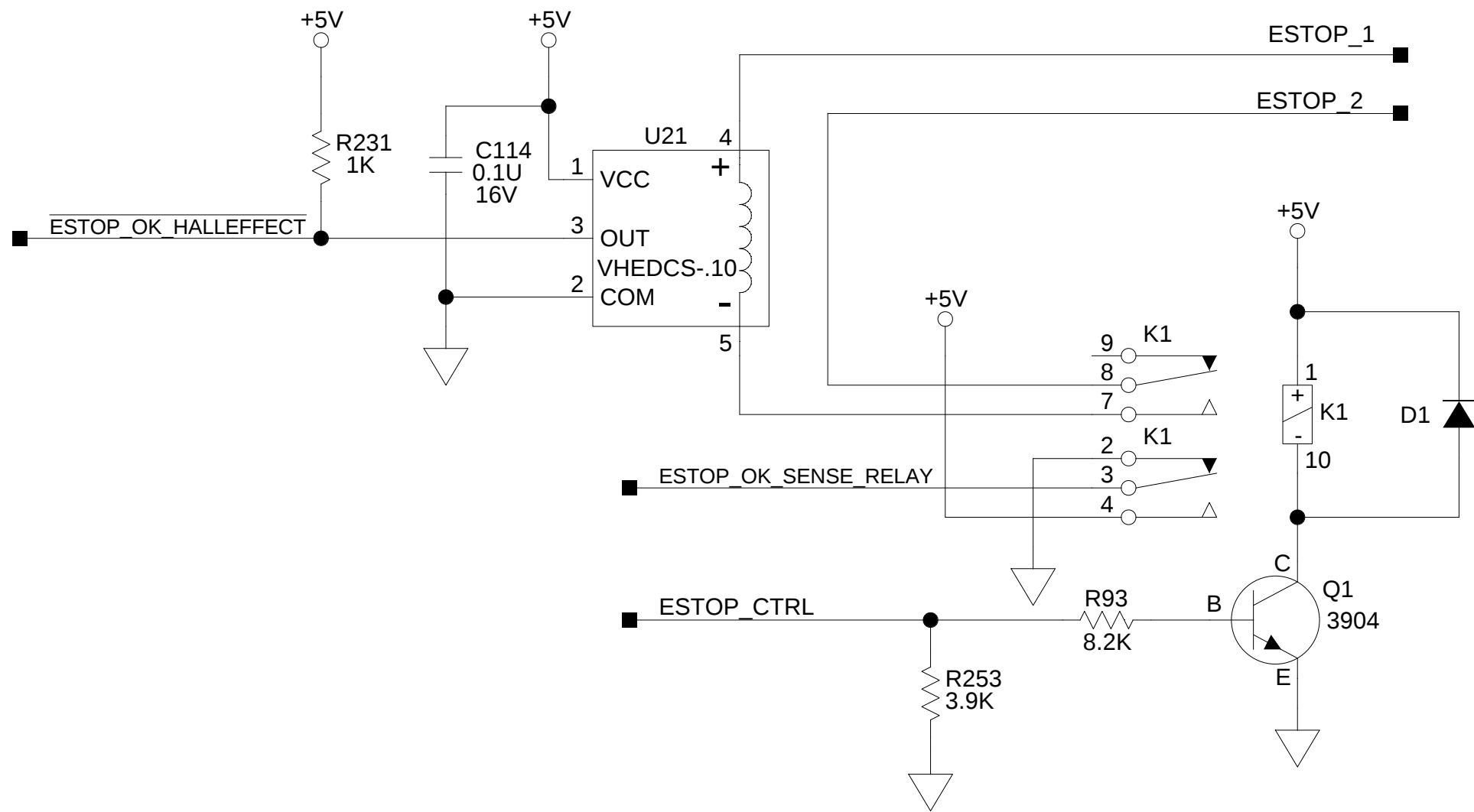
DPRAM DECOUPLING



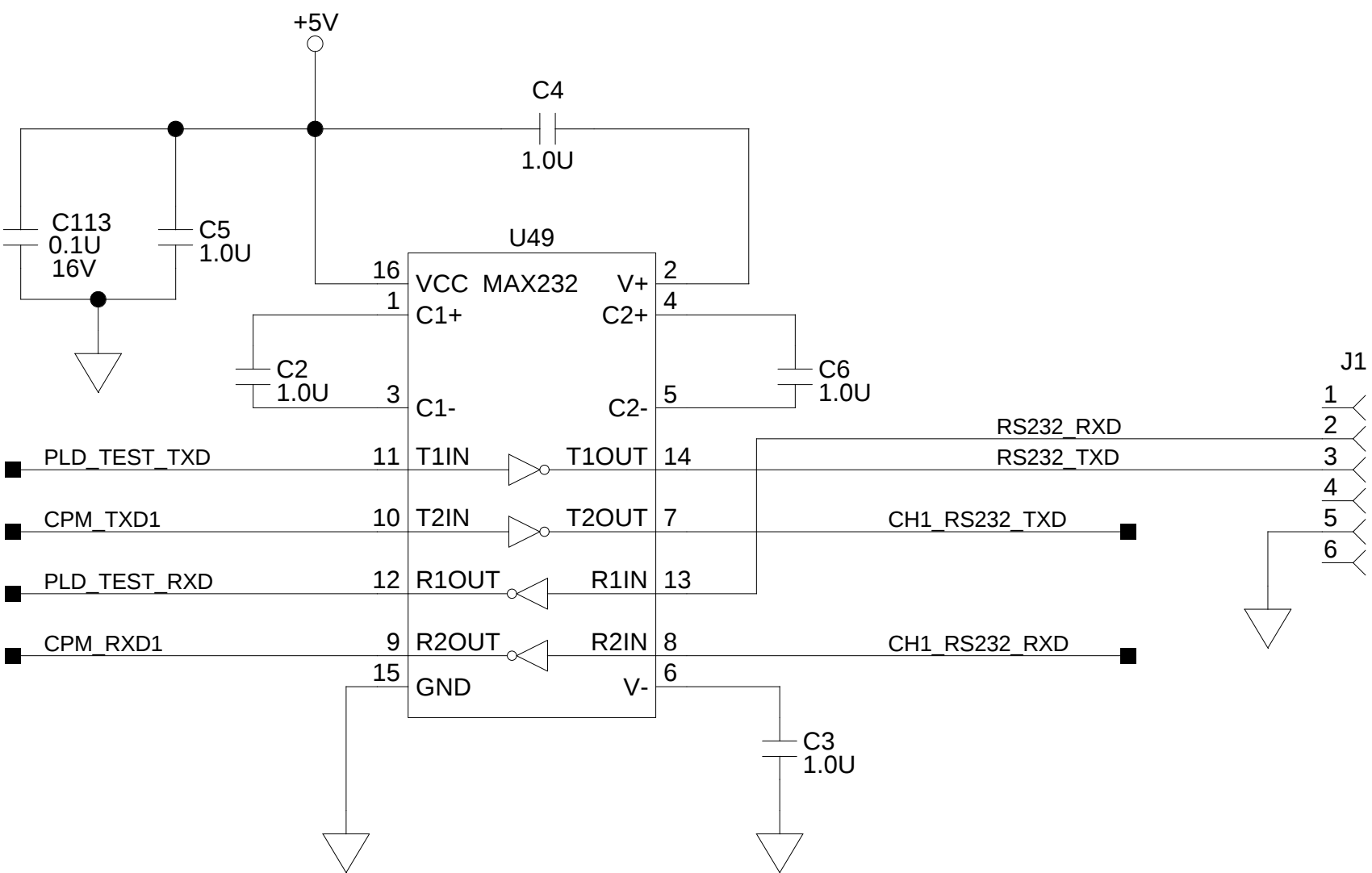
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DO NOT SCALE		SHEET	16 OF 20	REV

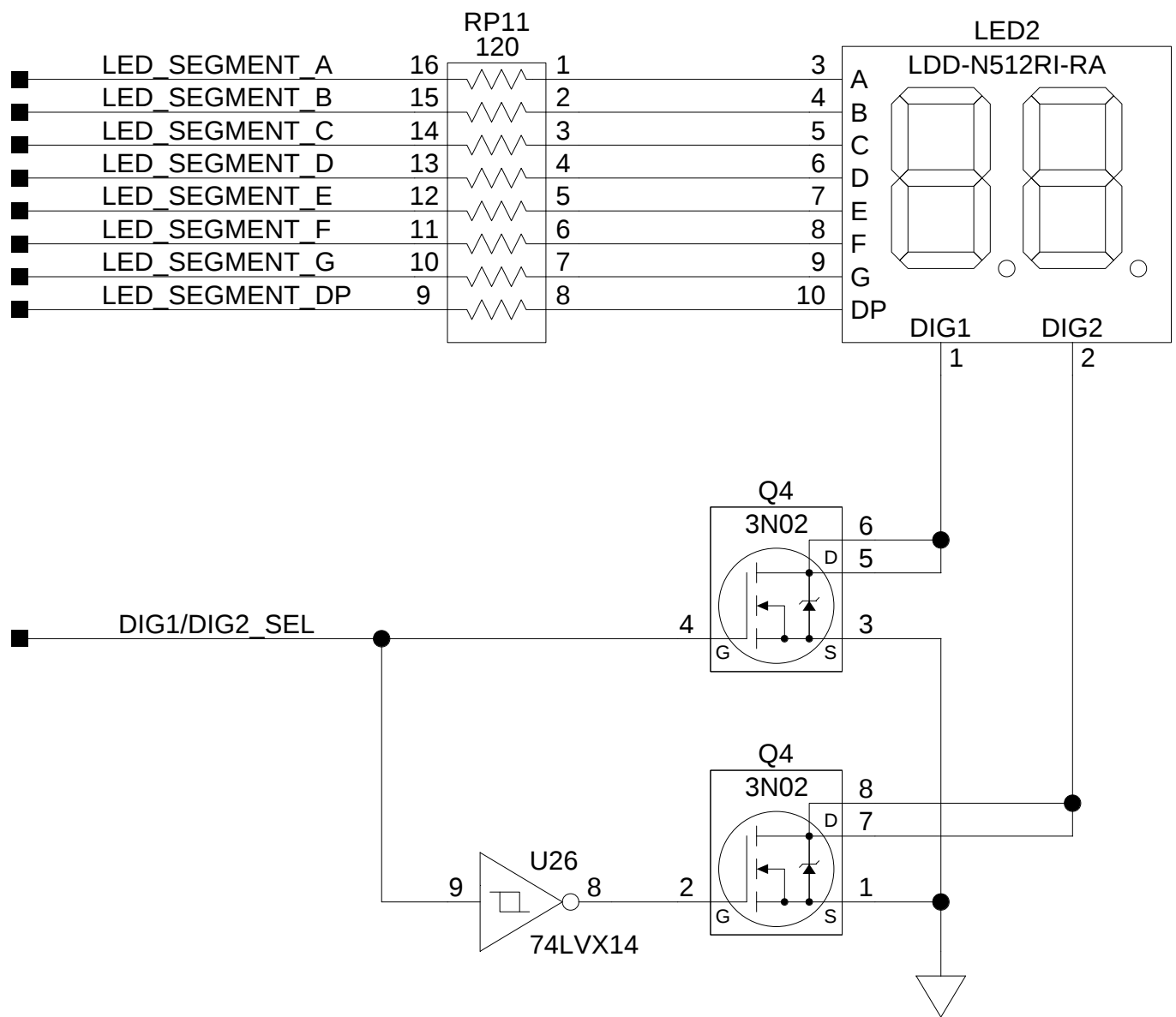
ESTOP INTERFACE



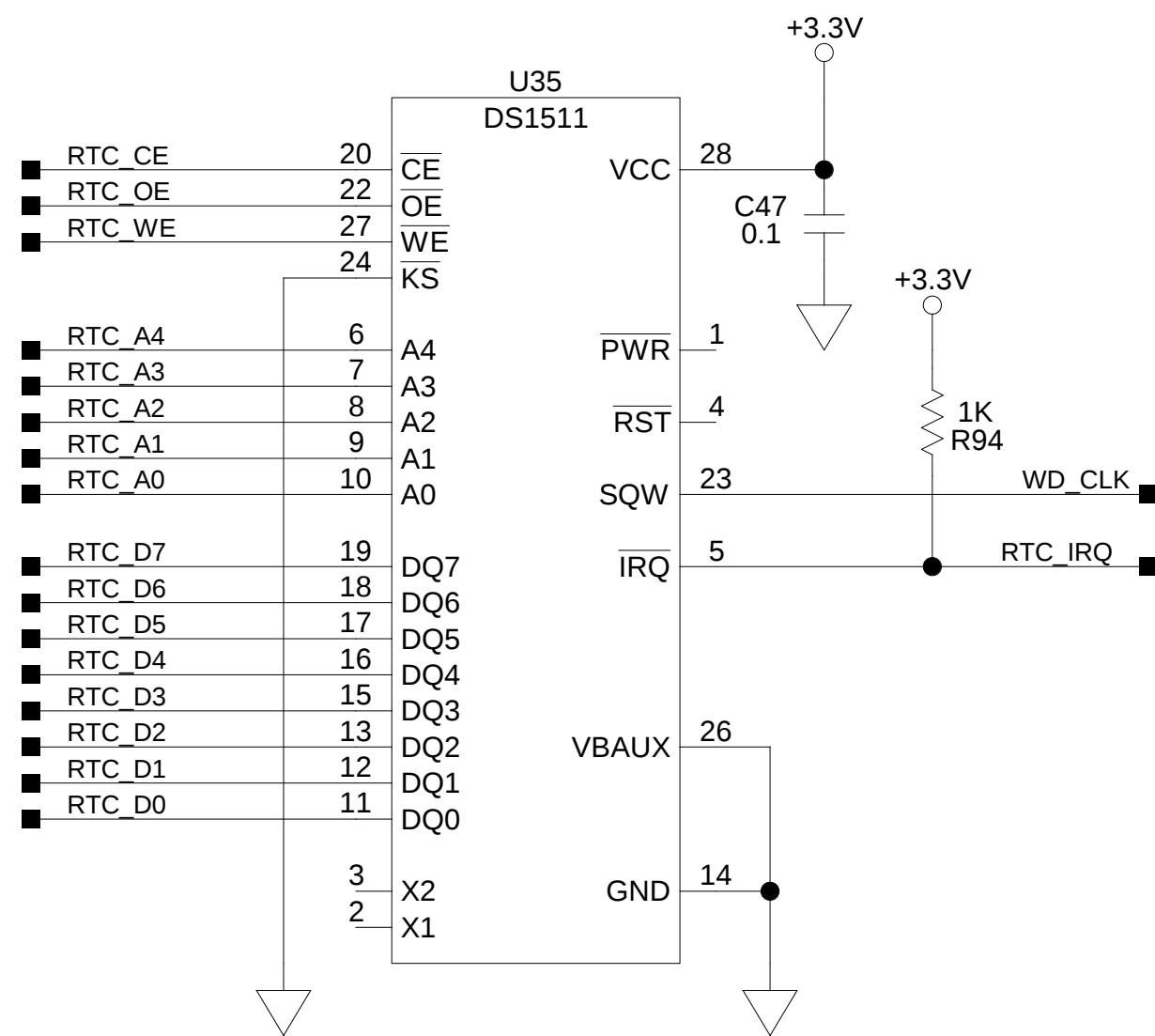
SERIAL PORTS



ERROR/STATUS CODE LEDS



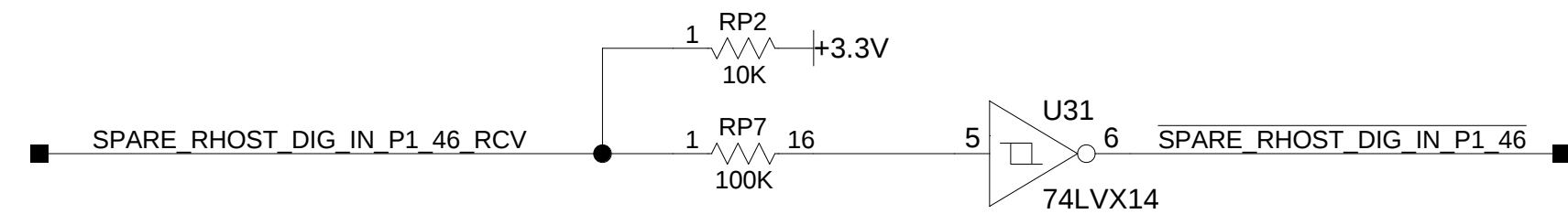
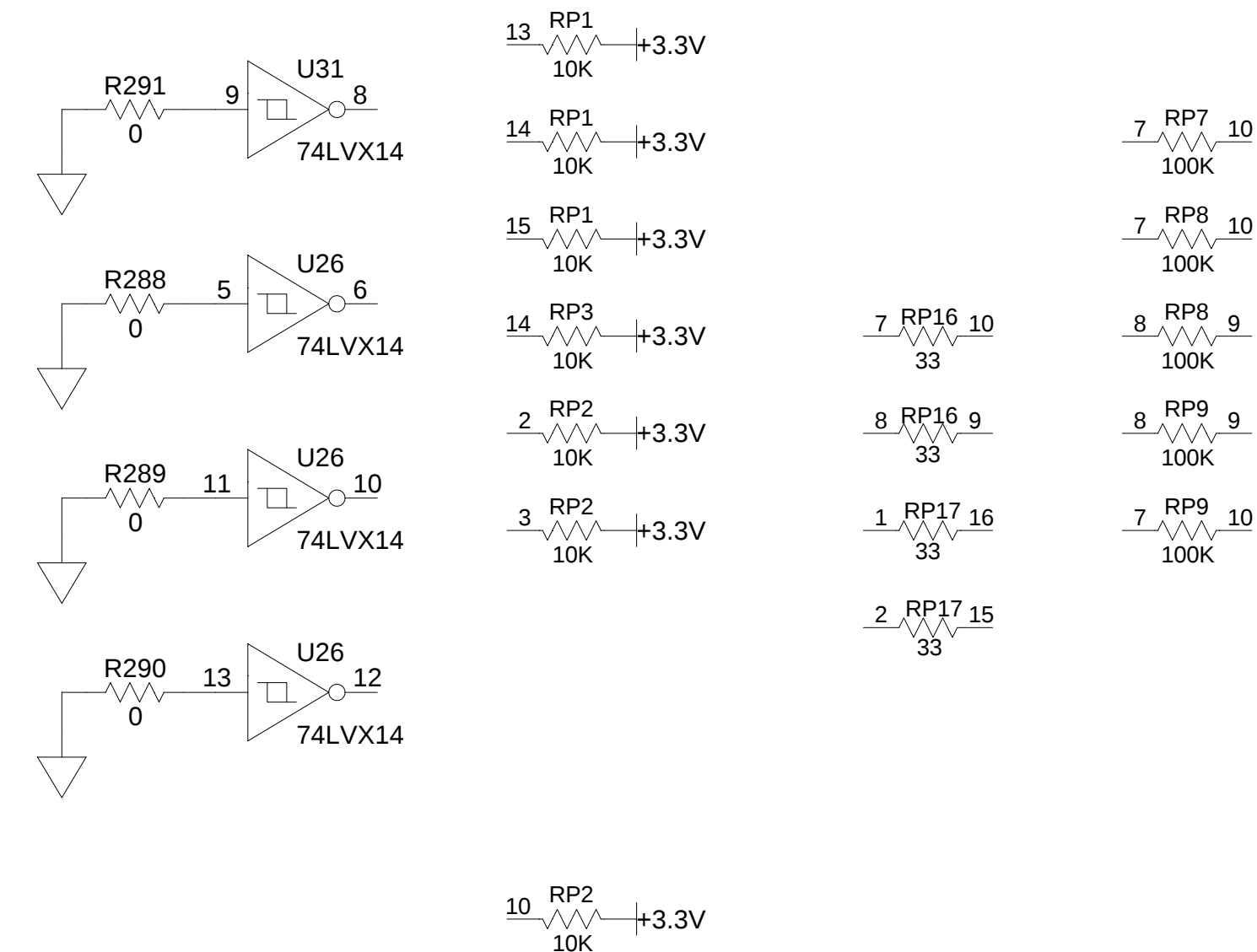
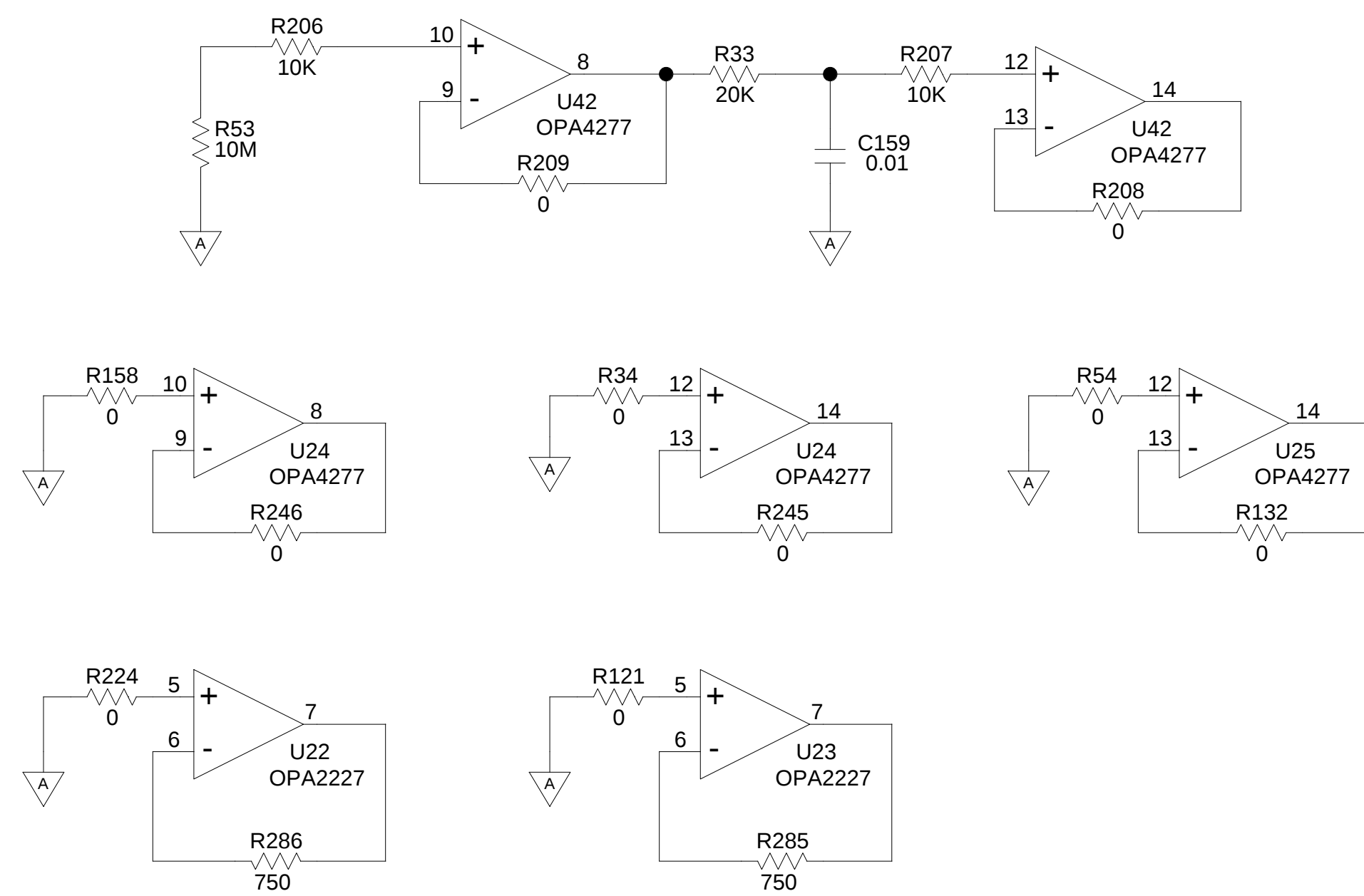
REAL TIME CLOCK



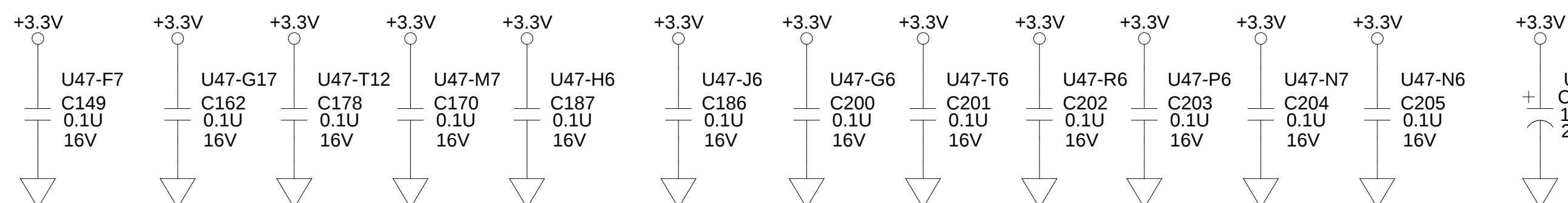
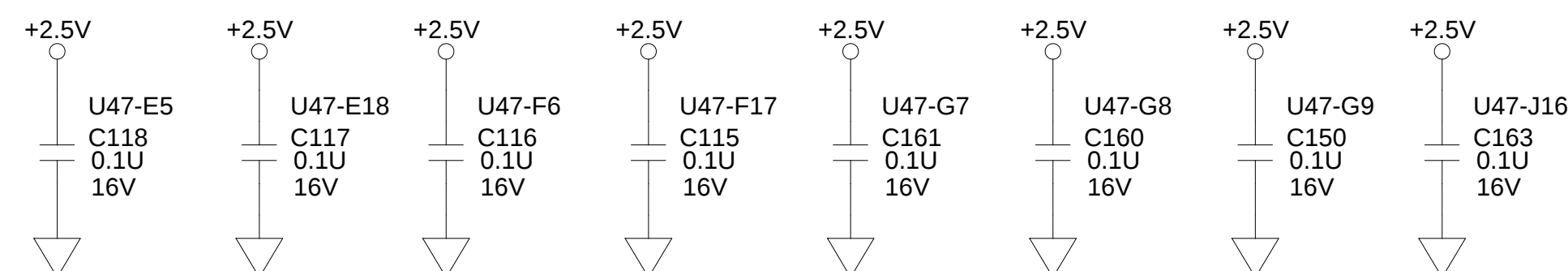
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DWG SCALE	NONE			
DO NOT SCALE				

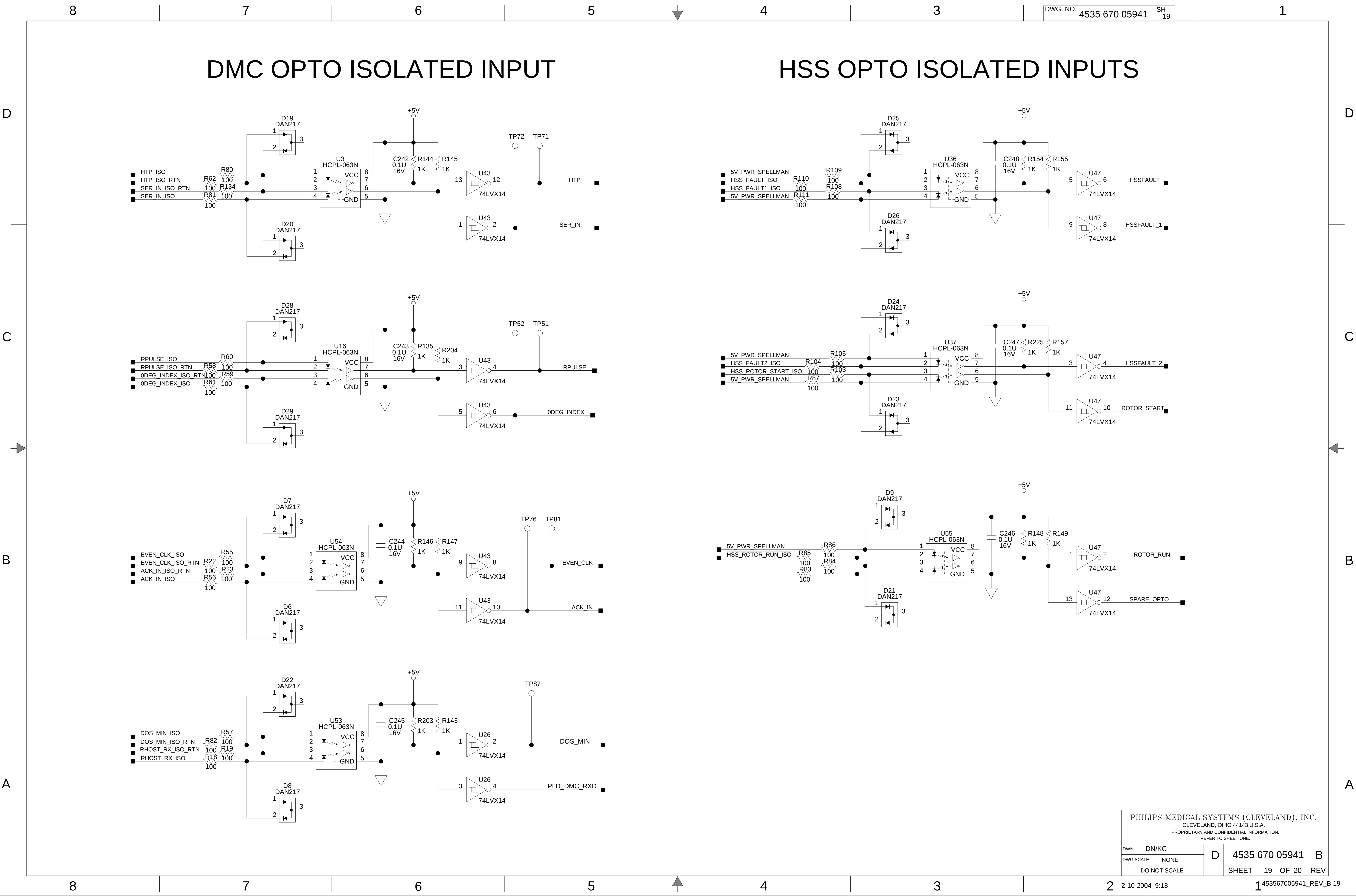
SPARES



FPGA DECOUPLING



U10	
A2	XC2S150-FG456
A6	NC1
A16	NC2
A13	NC3
A14	NC4
B11	NC5
B16	NC6
C2	NC7
C8	NC8
C9	NC9
D1	NC10
D4	NC11
D18	NC12
D19	NC13
E13	NC14
E17	NC15
E19	NC16
F11	NC17
G2	NC18
G22	NC19
H21	NC20
J1	NC21
J4	NC22
K2	NC23
K18	NC24
K19	NC25
L2	NC26
L17	NC27
M2	NC28
	NC29



D

C

B

A

D

C

B

A

CAN BUS INTERFACE

